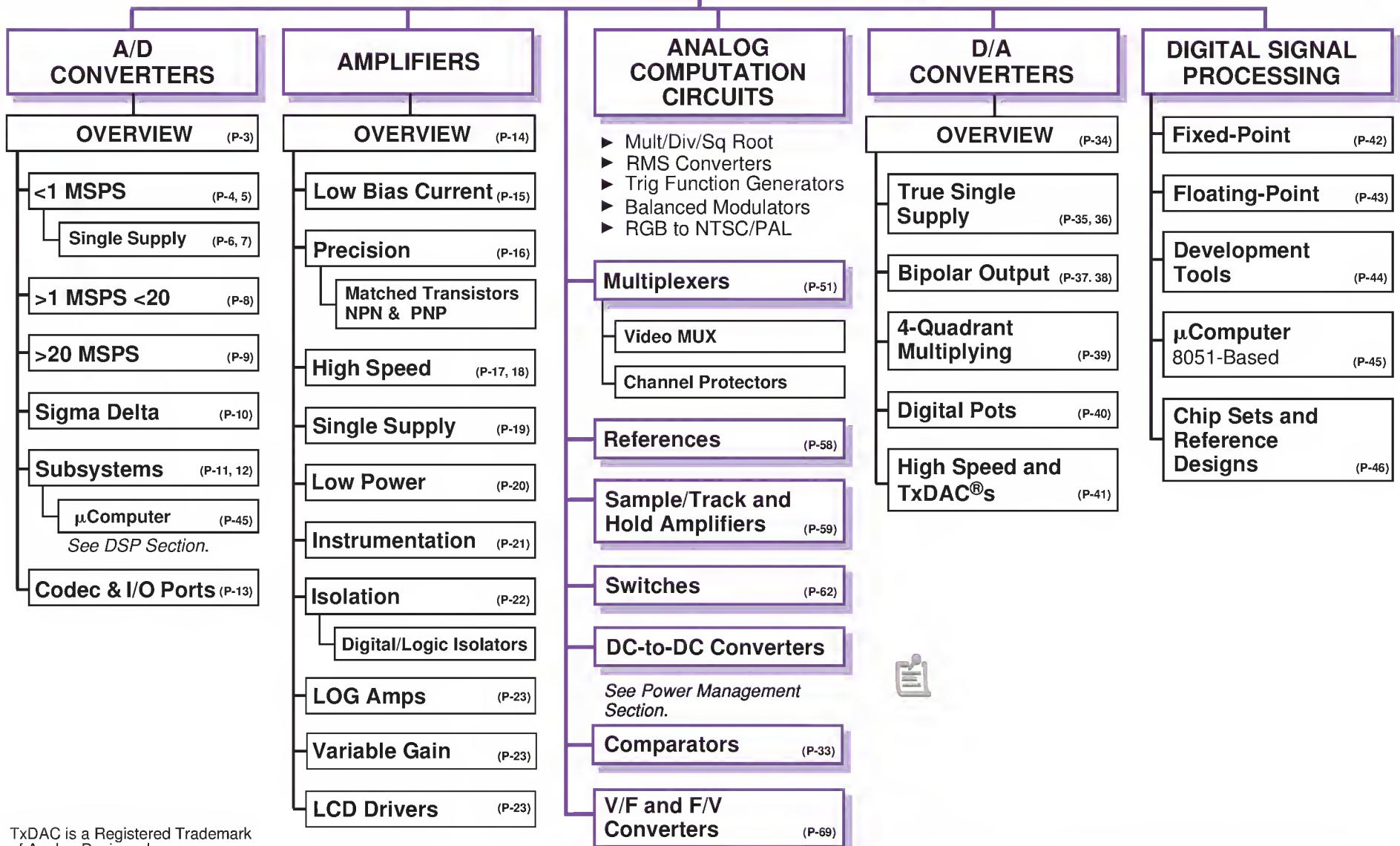


GENERAL PURPOSE COMPONENTS

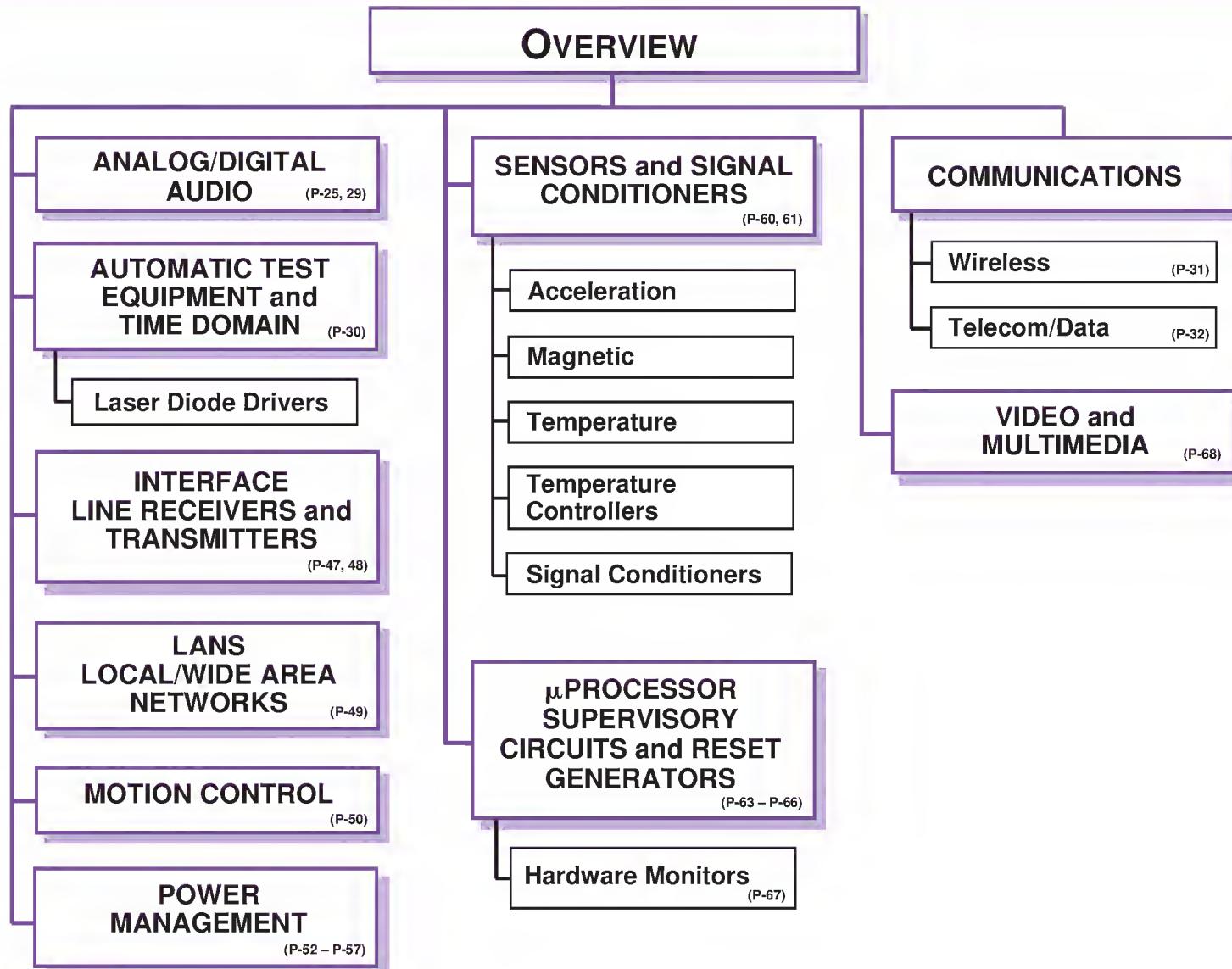
OVERVIEW



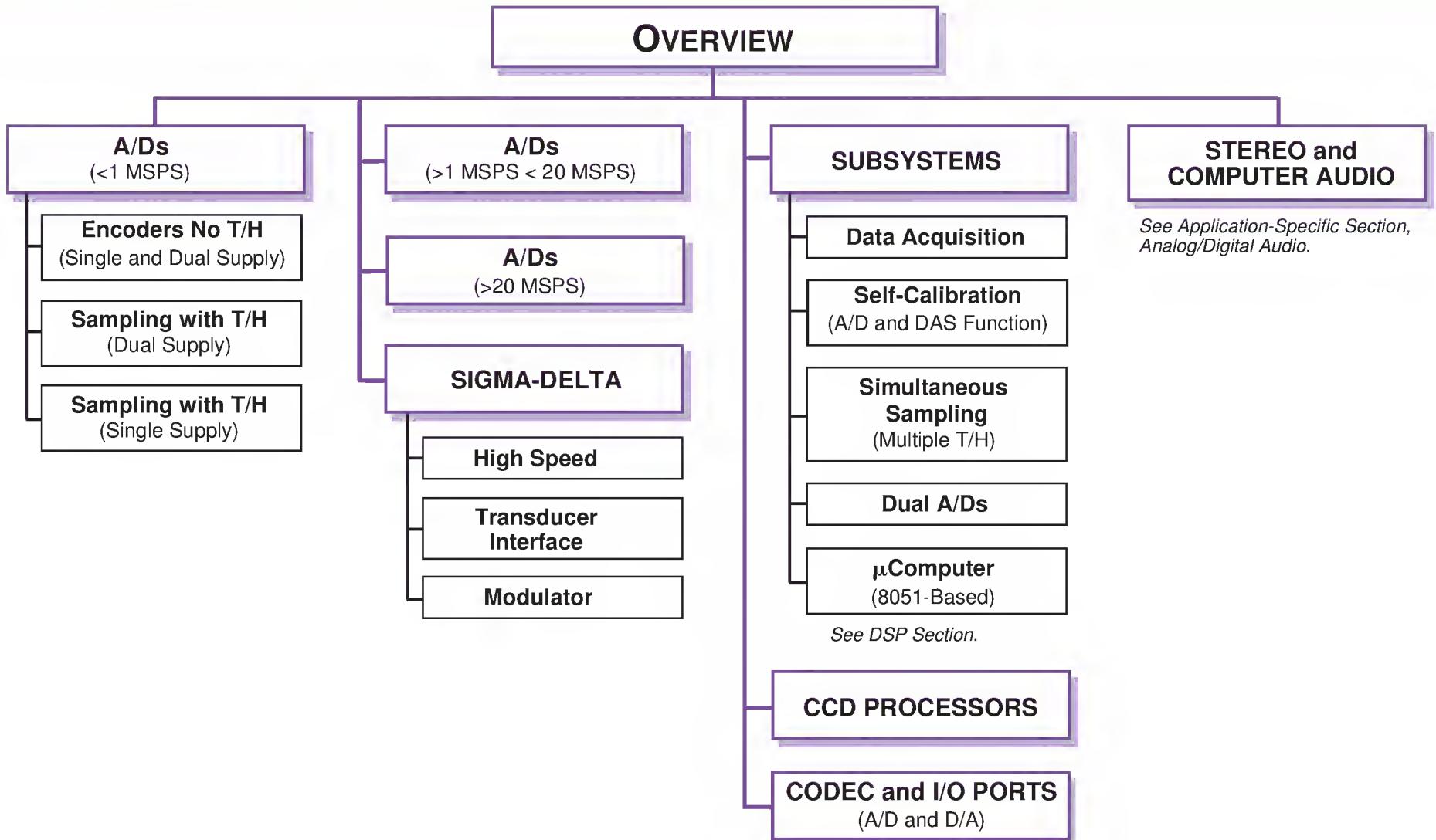
TxDAC is a Registered Trademark
of Analog Devices, Inc.



APPLICATION SPECIFIC CIRCUITS

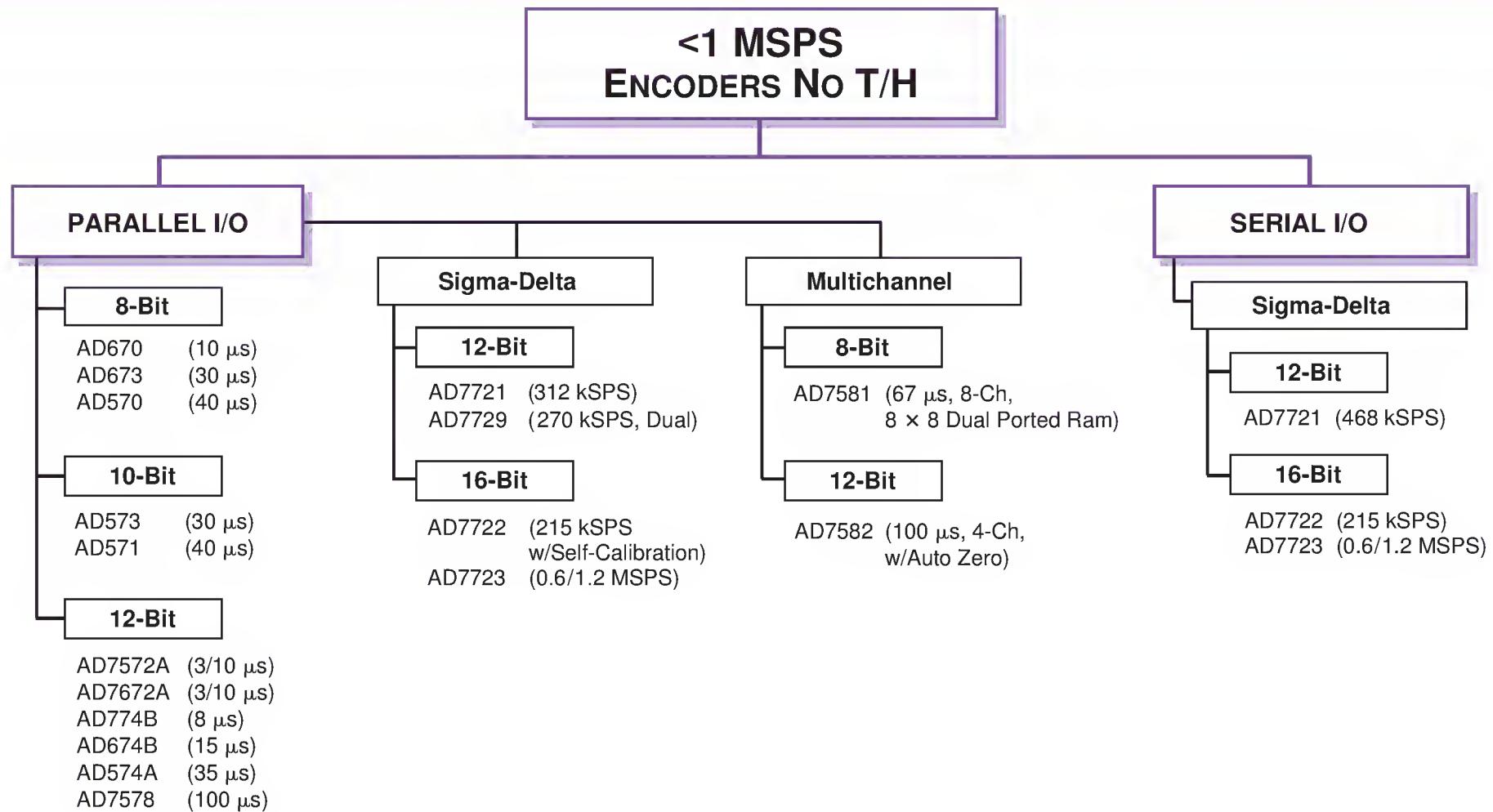


A/D CONVERTERS

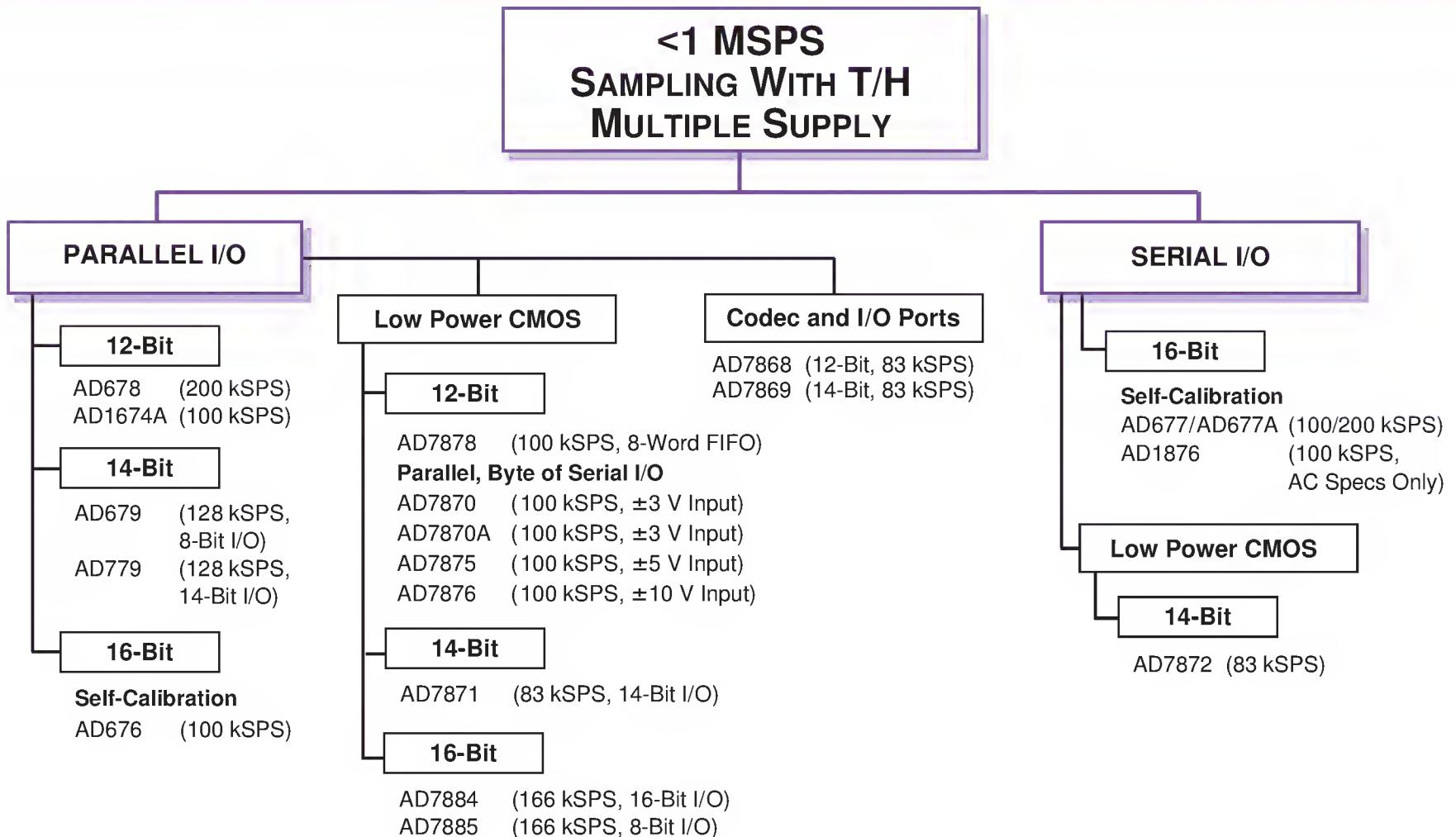


See DSP Section.

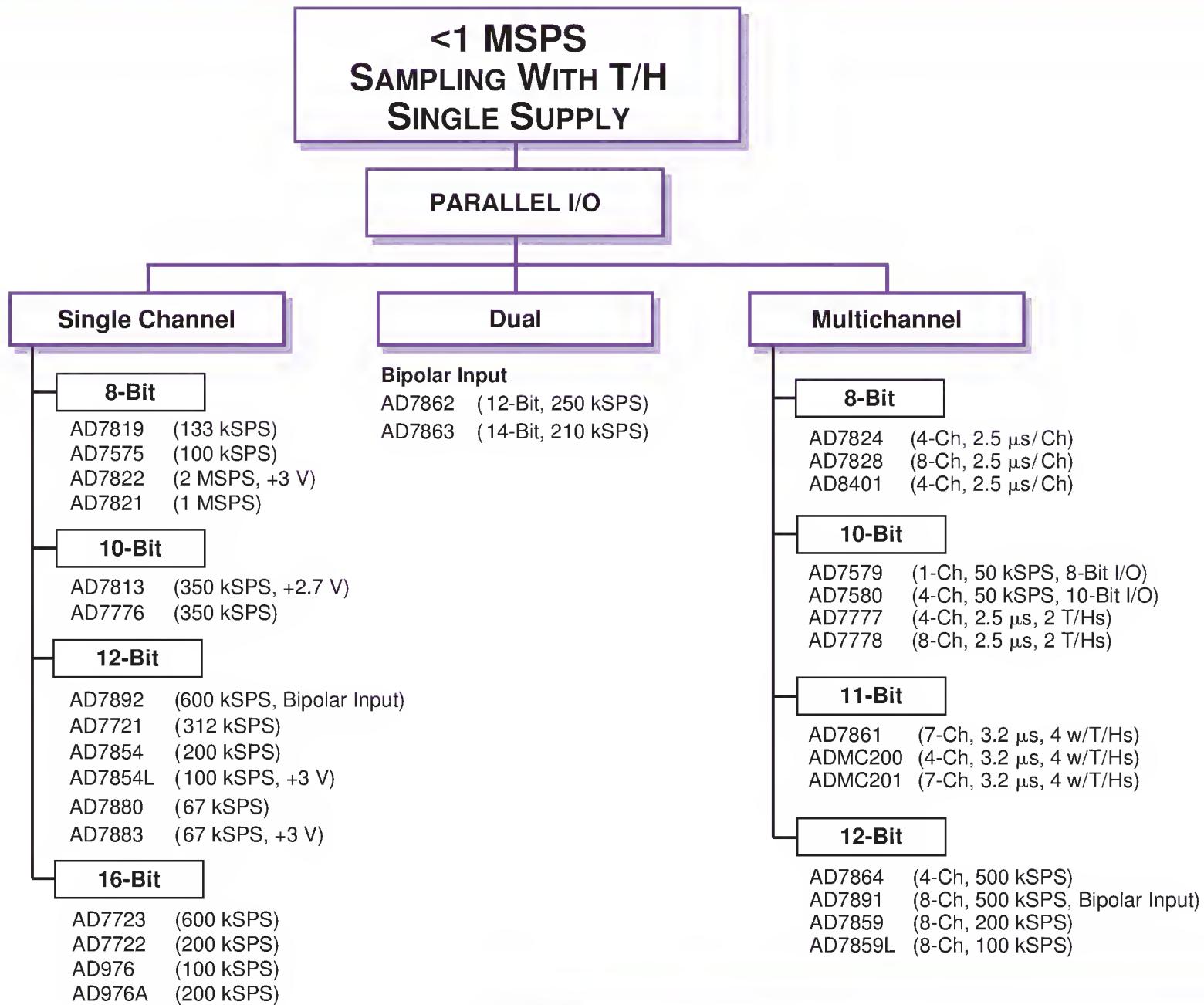
A/D CONVERTERS



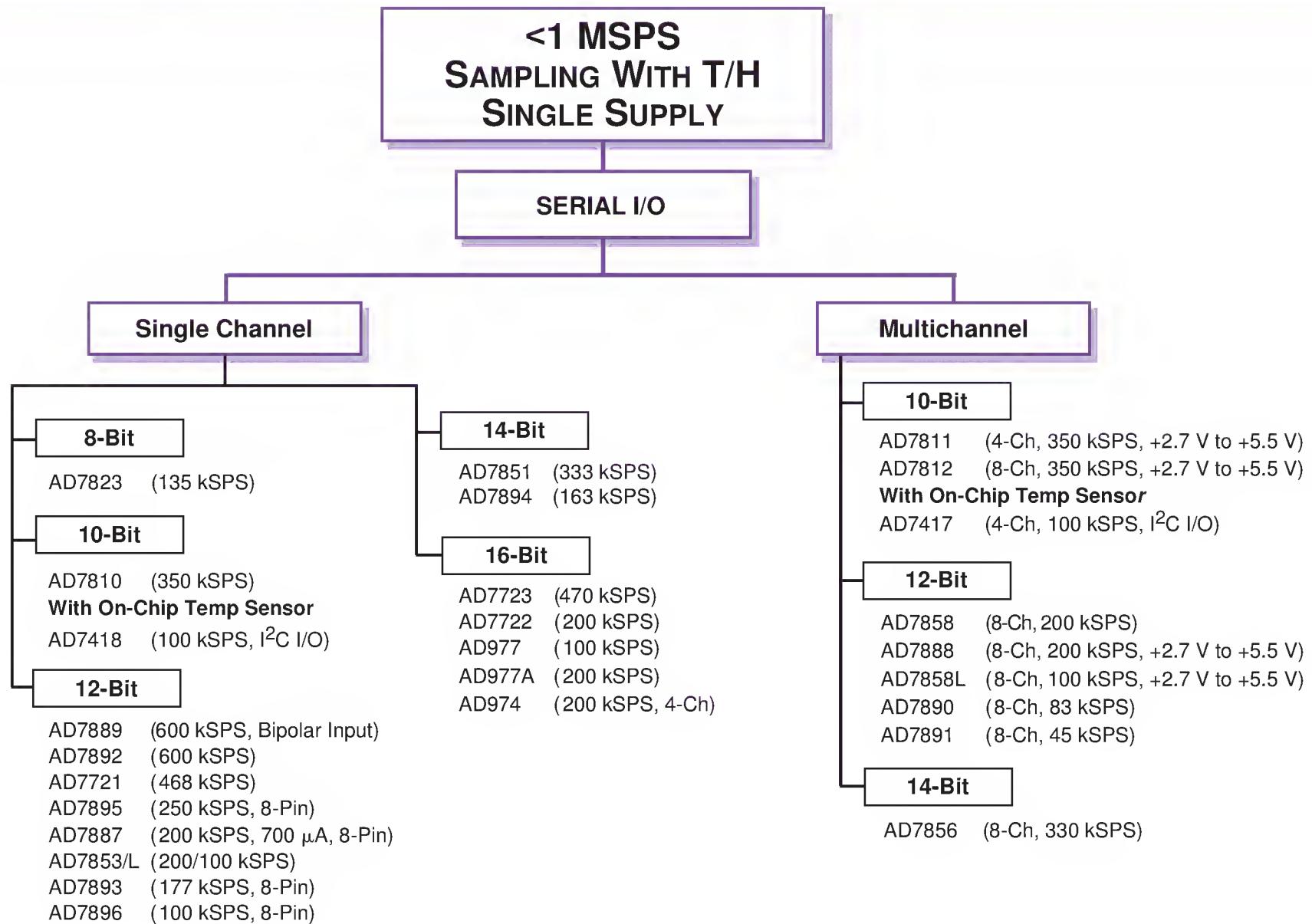
A/D CONVERTERS



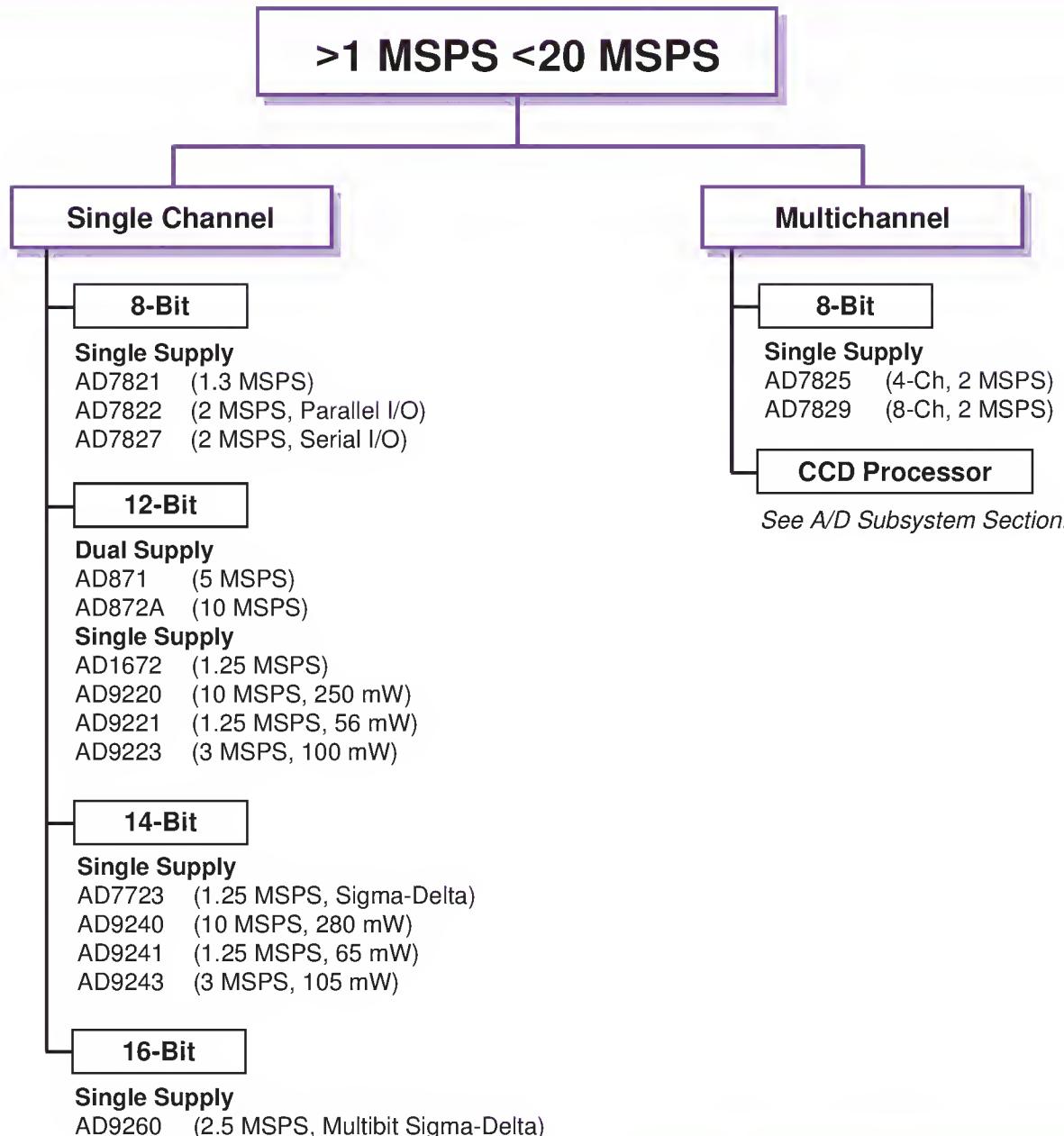
A/D CONVERTERS



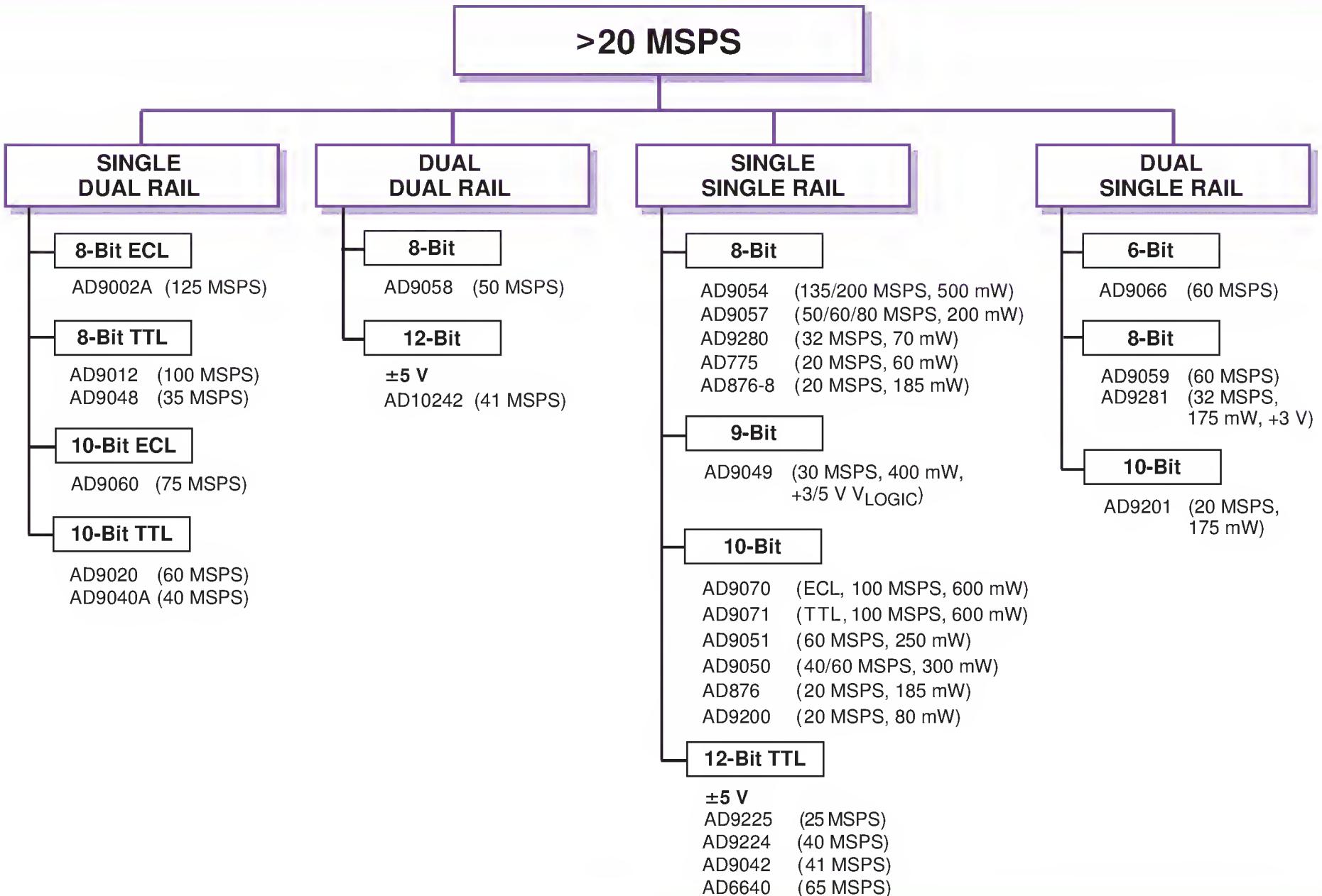
A/D CONVERTERS



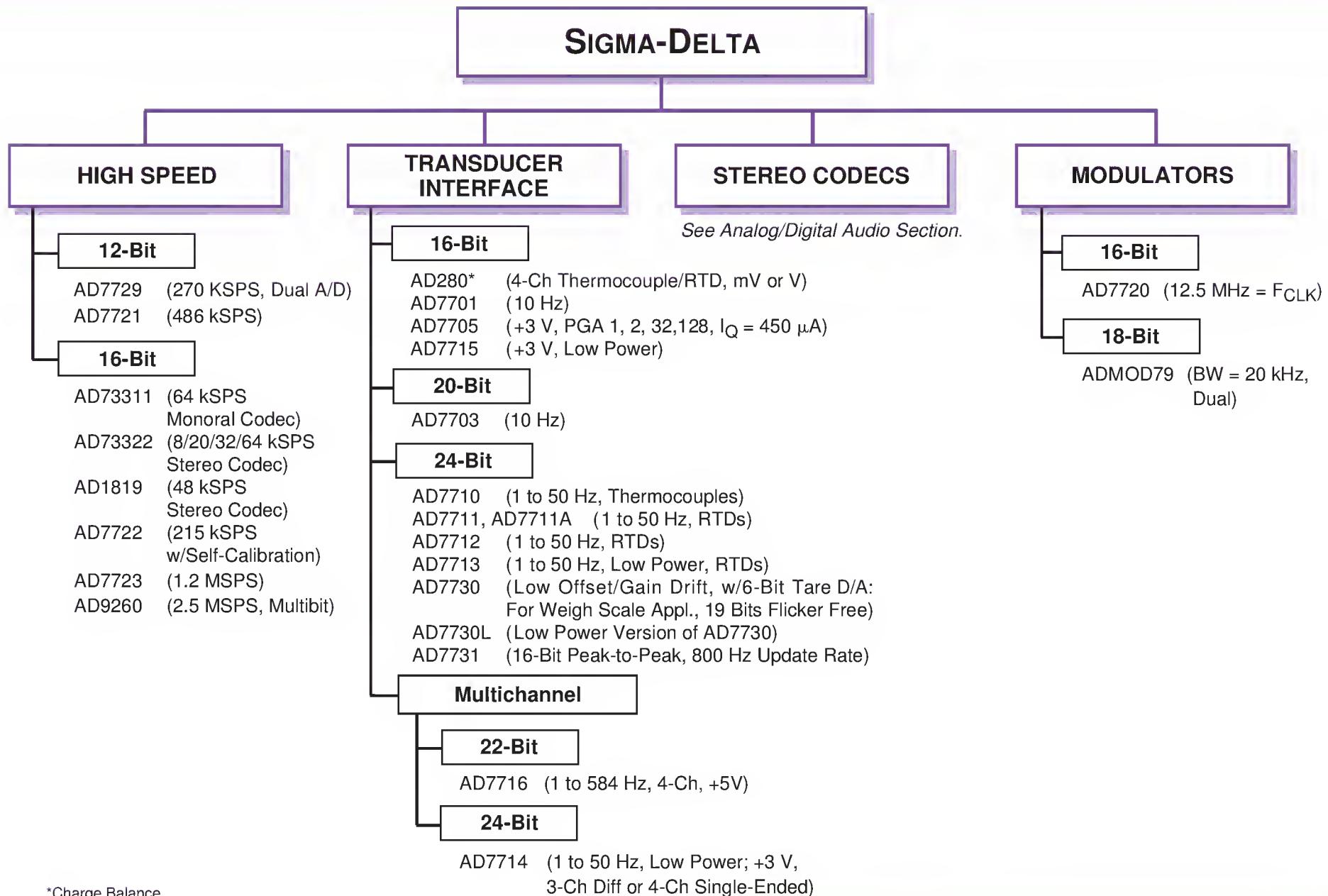
A/D CONVERTERS



A/D CONVERTERS

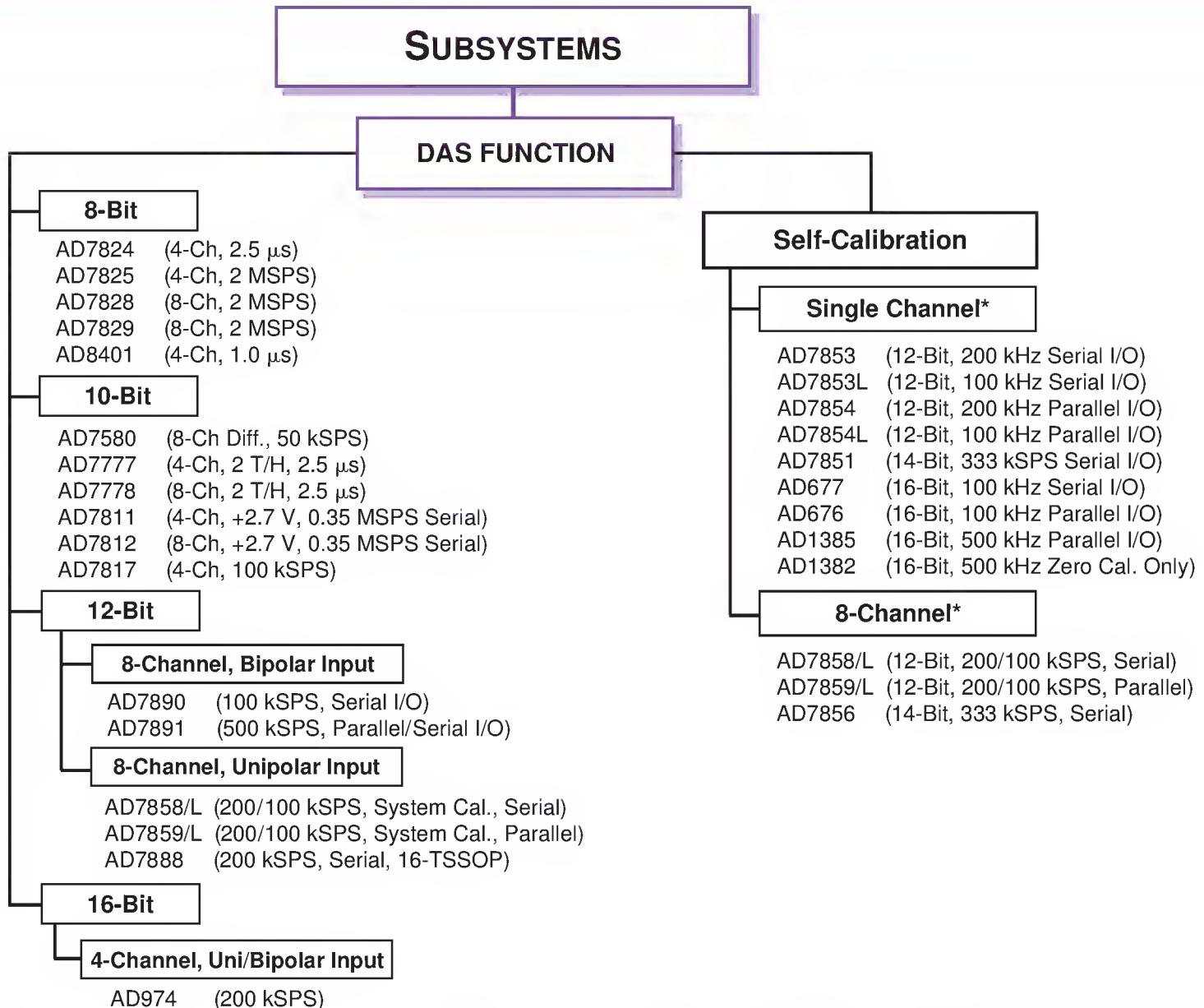


A/D CONVERTERS



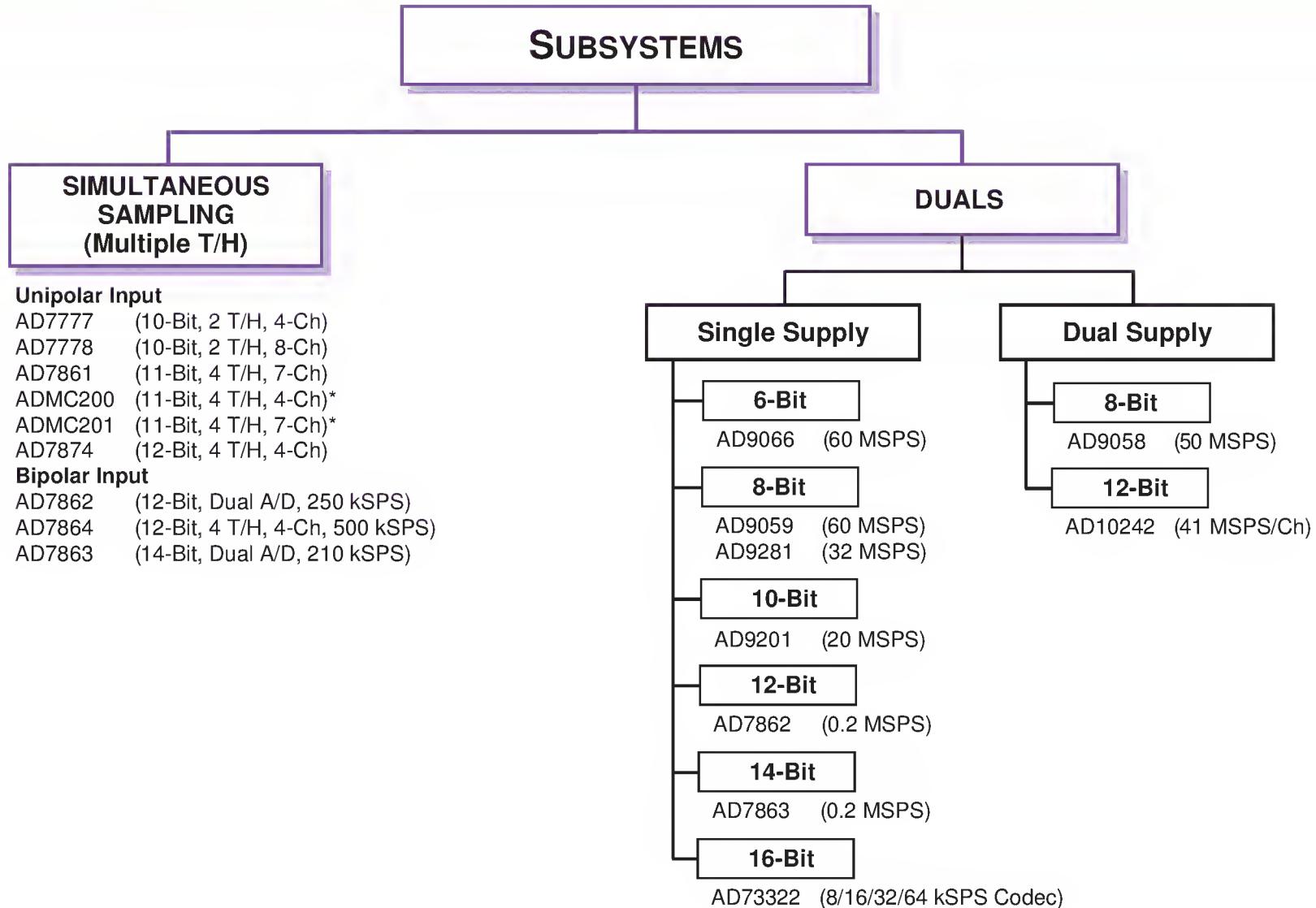
*Charge Balance

A/D CONVERTERS



*L Version +3.0 V Operation.

A/D CONVERTERS



*L Version +3.0 V Operation.

A/D CONVERTERS

CCD SIGNAL PROCESSORS

1-Channel

AD9801 (10-Bit, 18 MSPS)
AD9802 (10-Bit, 18 MSPS)

3-Channel

AD9805/9807 (10/12-Bit, 6 MSPS)
w/Triple Correlated Double Sampler,
Digital Offset and Gain Control,
PGA Gain 1 to 4, 16 Steps

CODEC and I/O PORTS

8-Bit

High Speed, 8-Bit A/D

AD7339 (Dual, 8-Bit D/A, Parallel I/O)
(Dual, 8-Bit D/A, Serial I/O)

500 kSPS

AD7569
AD7669 (Dual, 8-Bit D/A)
AD7769 (Dual, 8-Bit D/A)

12-Bit

AD7729 (270 kSPS, Dual A/D, 1 10-Bit D/A)
AD7868 (83 kSPS, 1 × A/D, 1 × D/A, Serial)

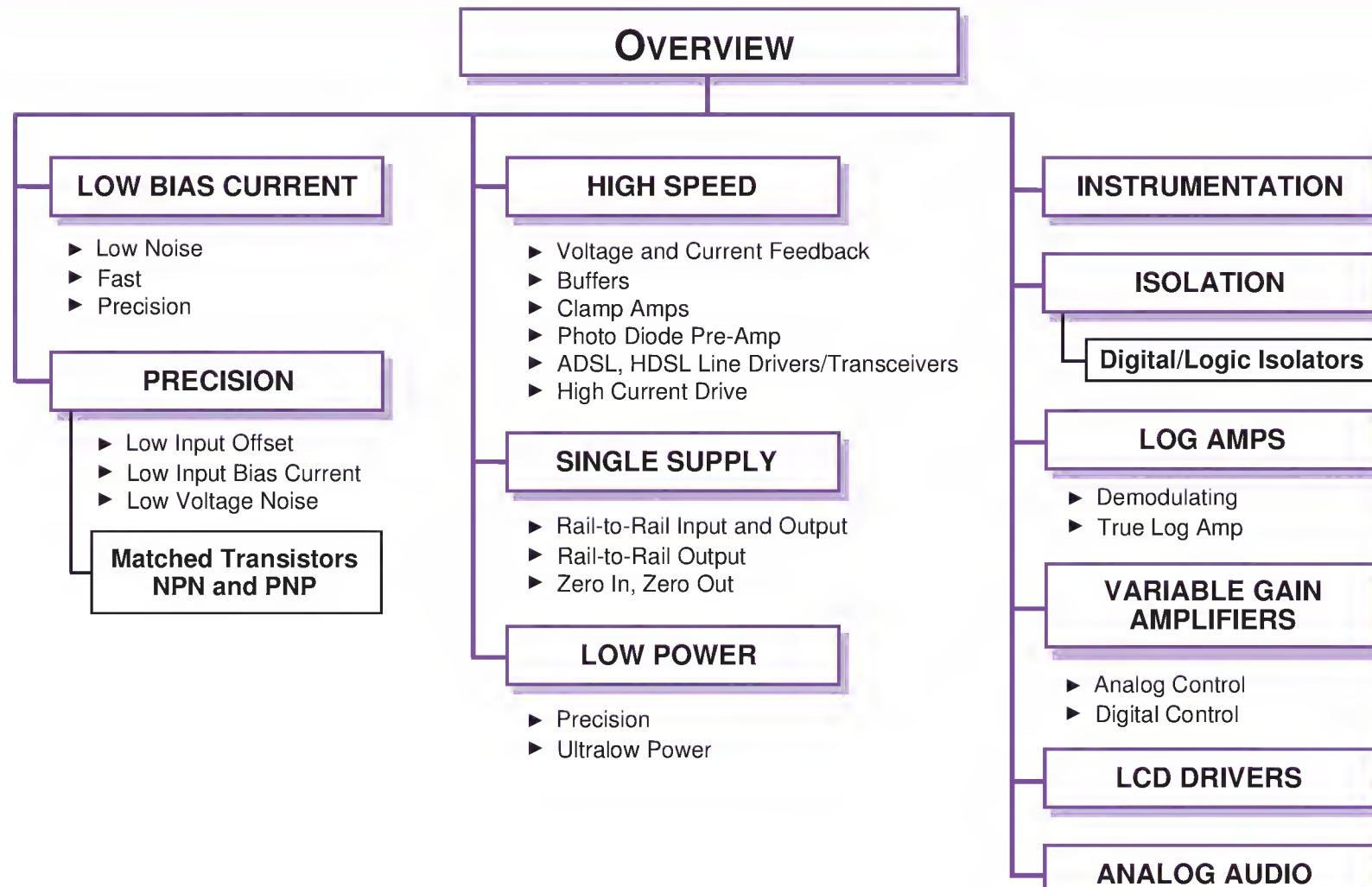
14-Bit

AD7868 (83 kSPS, 1 × A/D, 1 × D/A, Serial)

16-Bit

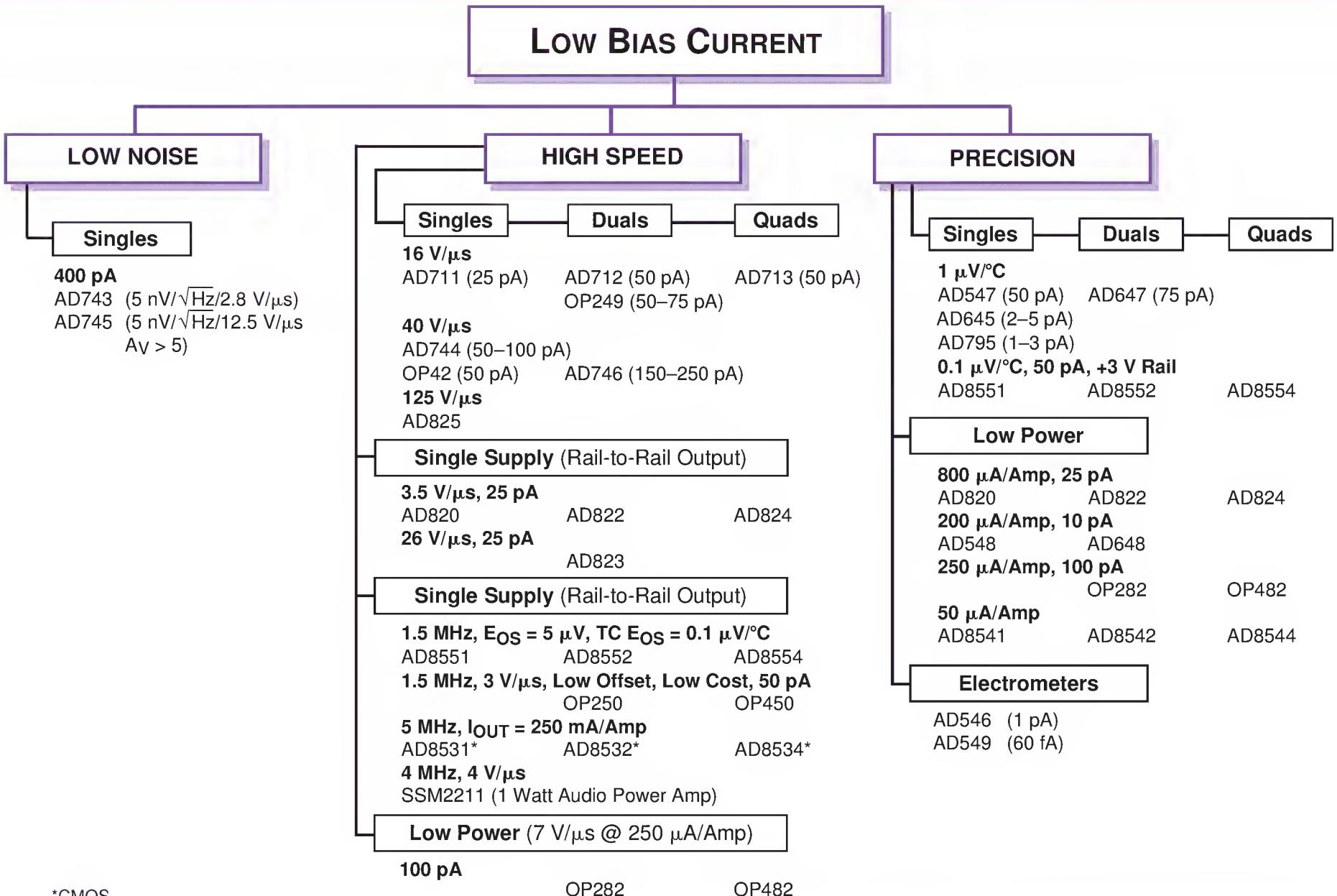
AD73311 (64 kSPS, Low Cost, Monaural Codec)
AD73322 (8/16/32/64 kSPS, Stereo Codec)

AMPLIFIERS



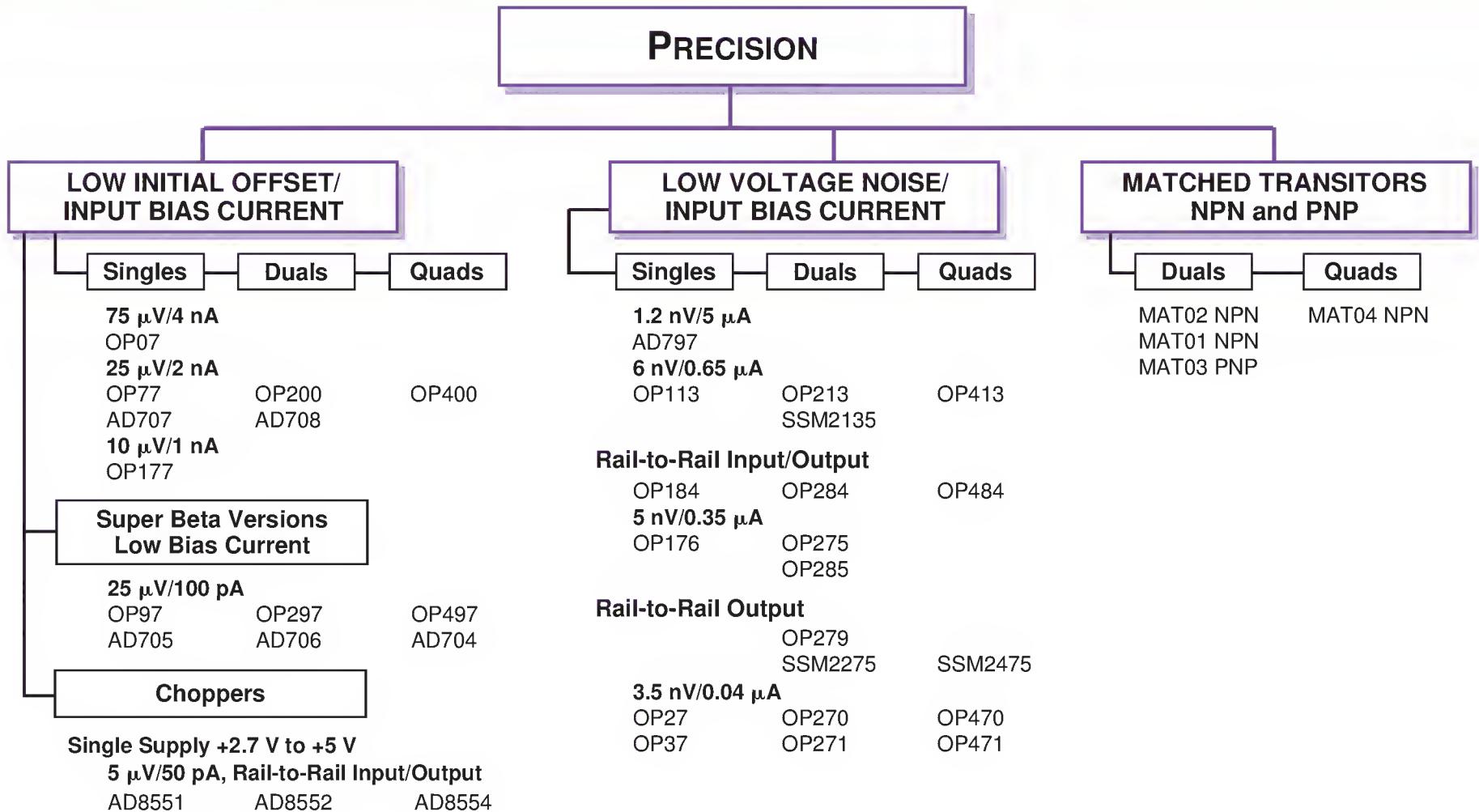
See Analog/Digital Audio Section.

AMPLIFIERS

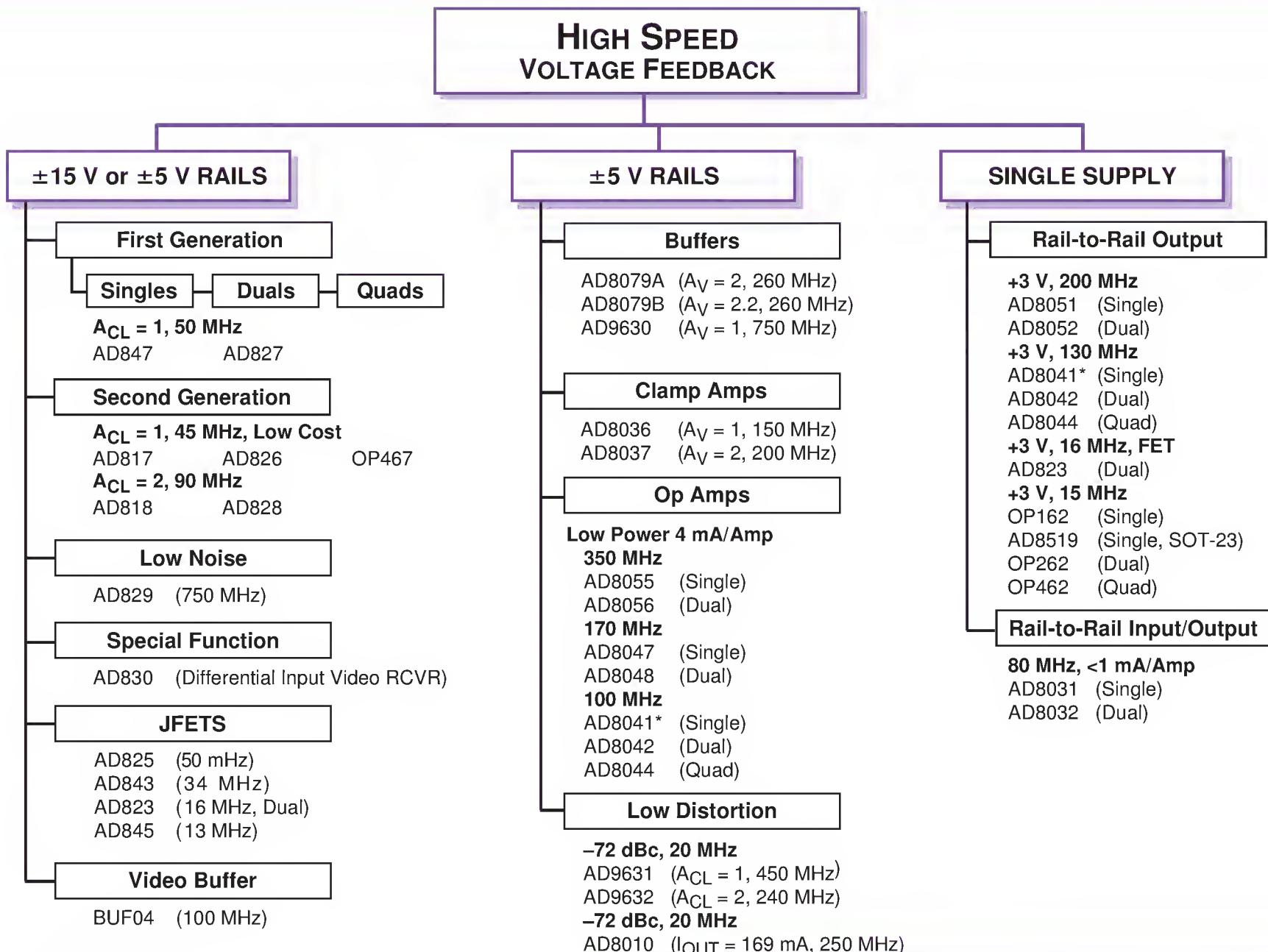


*CMOS

AMPLIFIERS

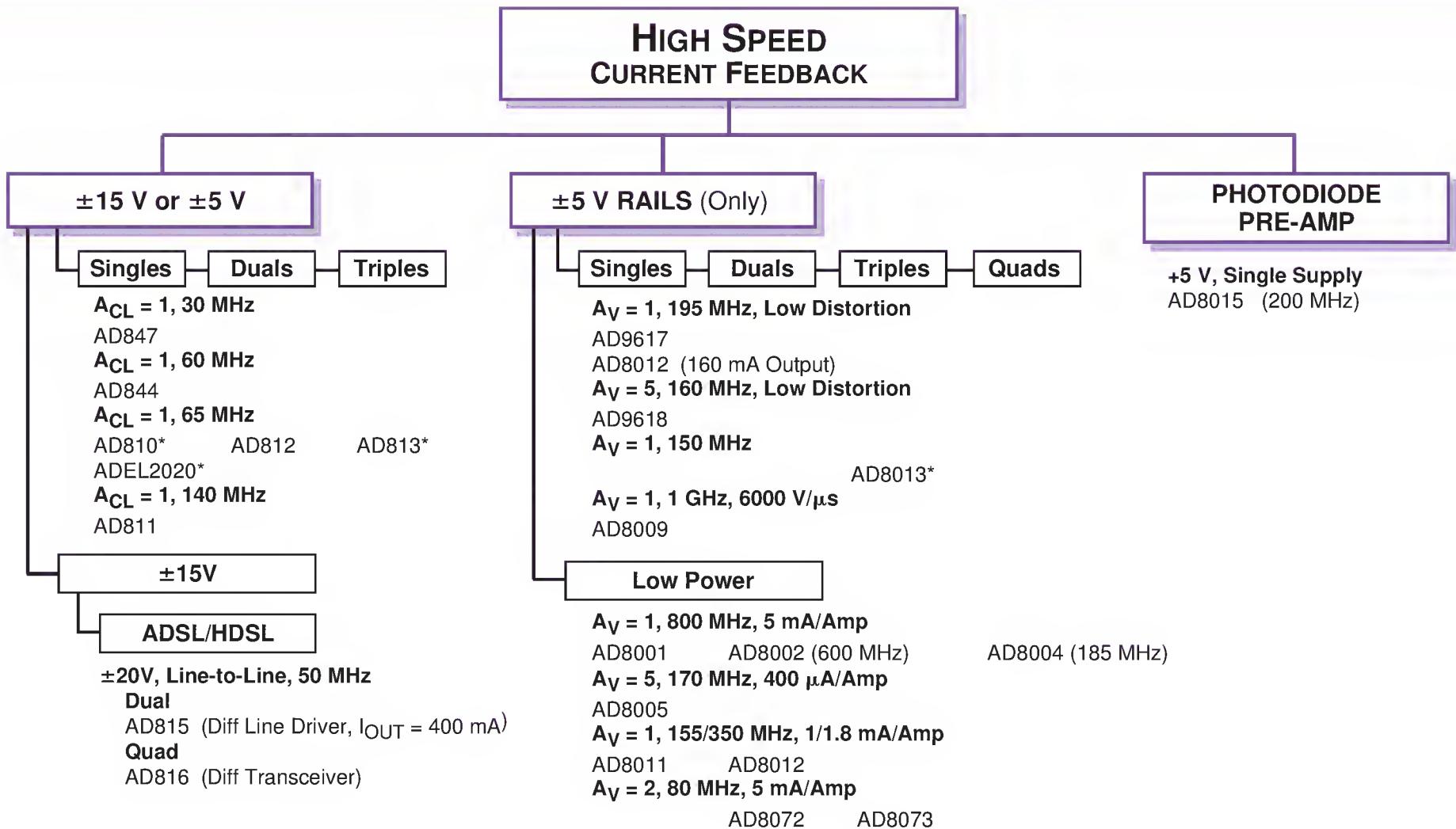


AMPLIFIERS



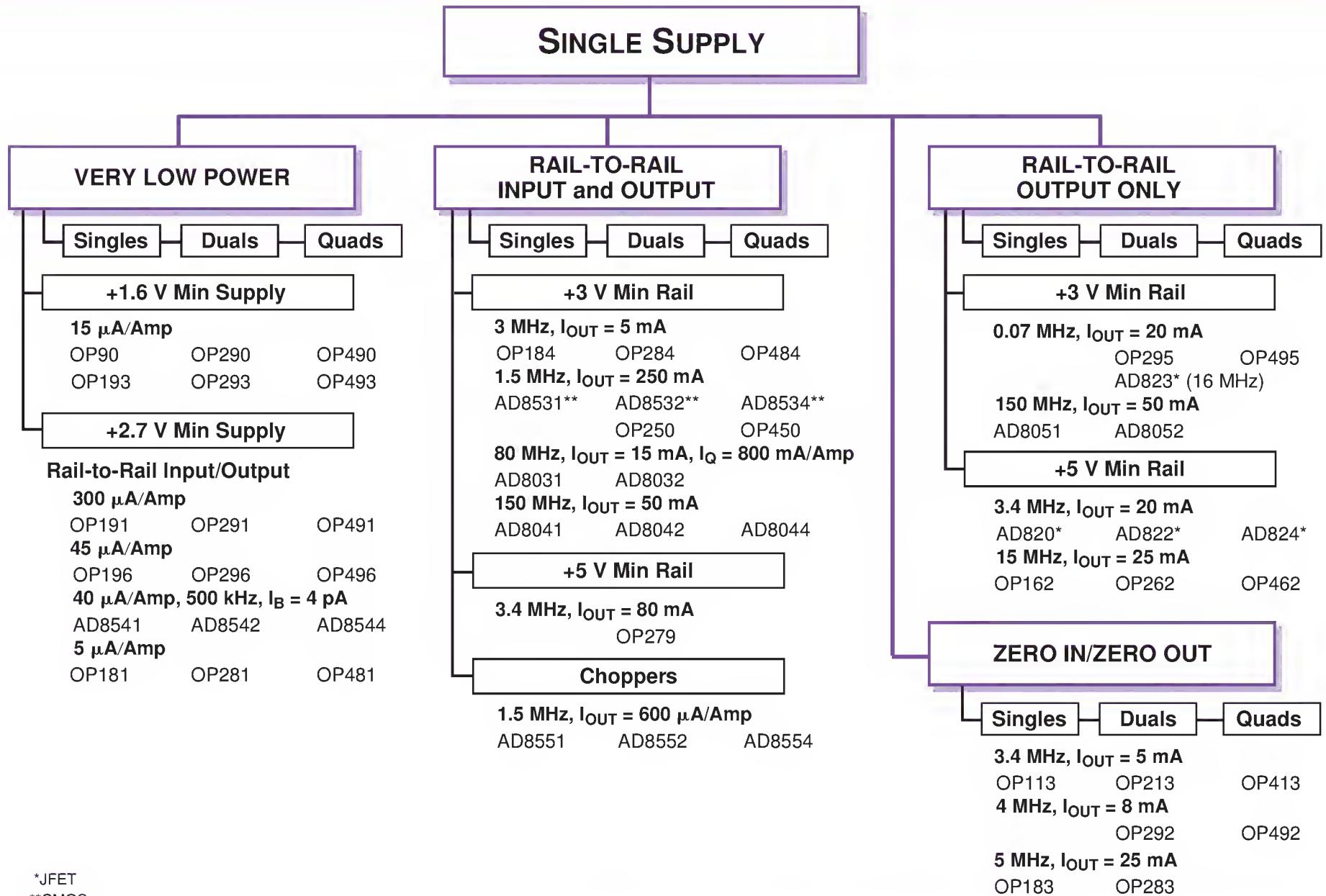
*Output Disable Function

AMPLIFIERS



*Output Disable Function

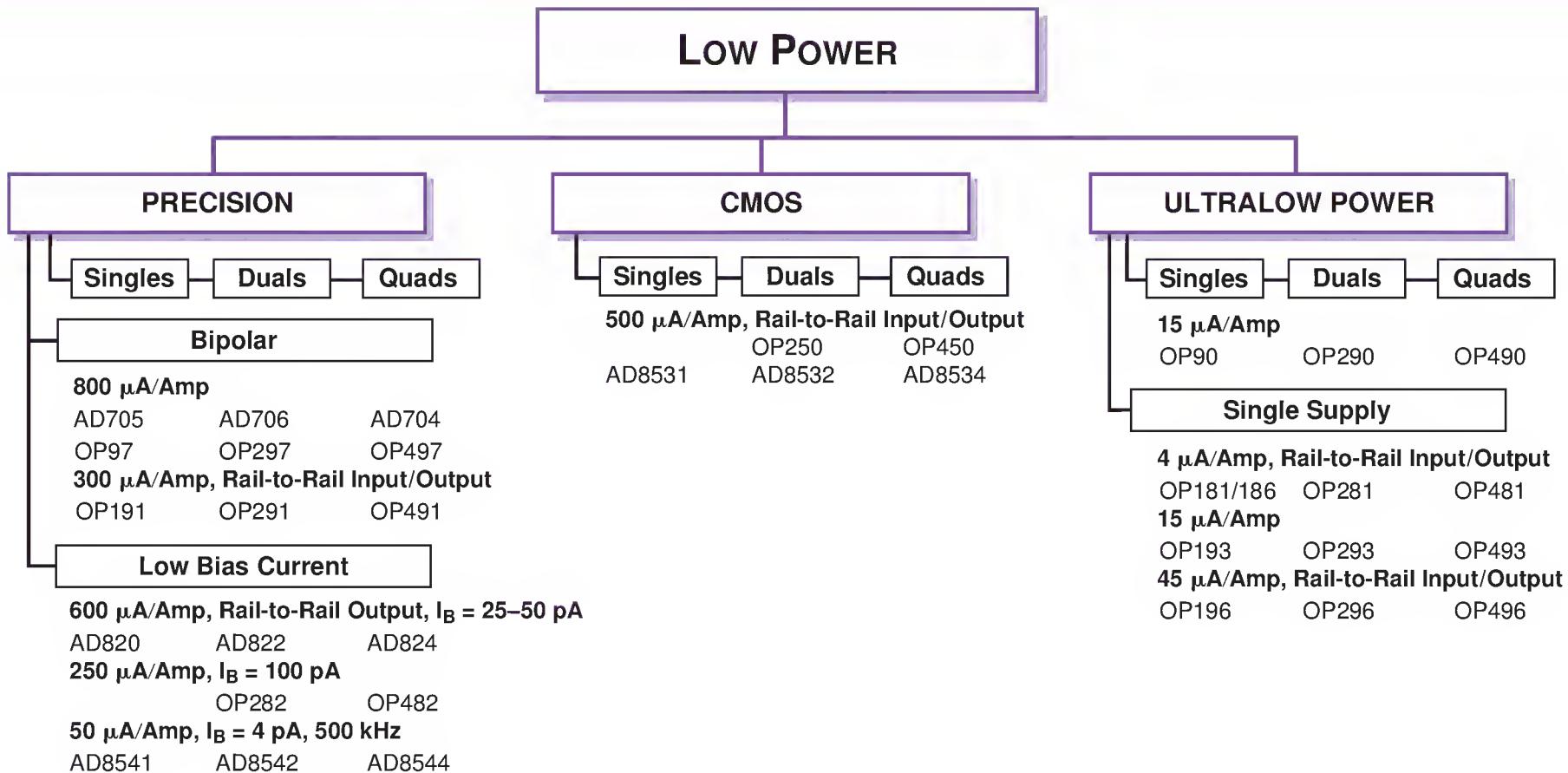
AMPLIFIERS



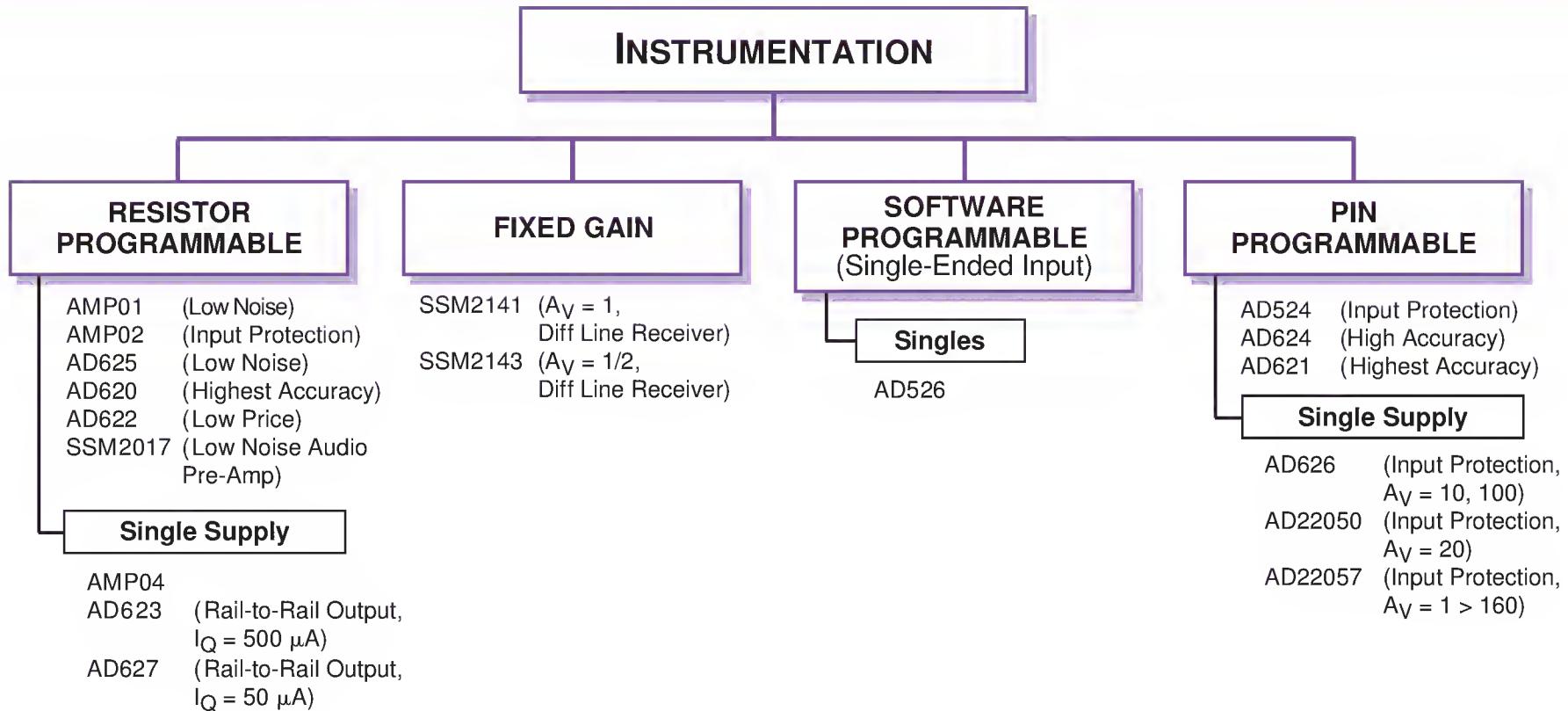
*JFET

**CMOS

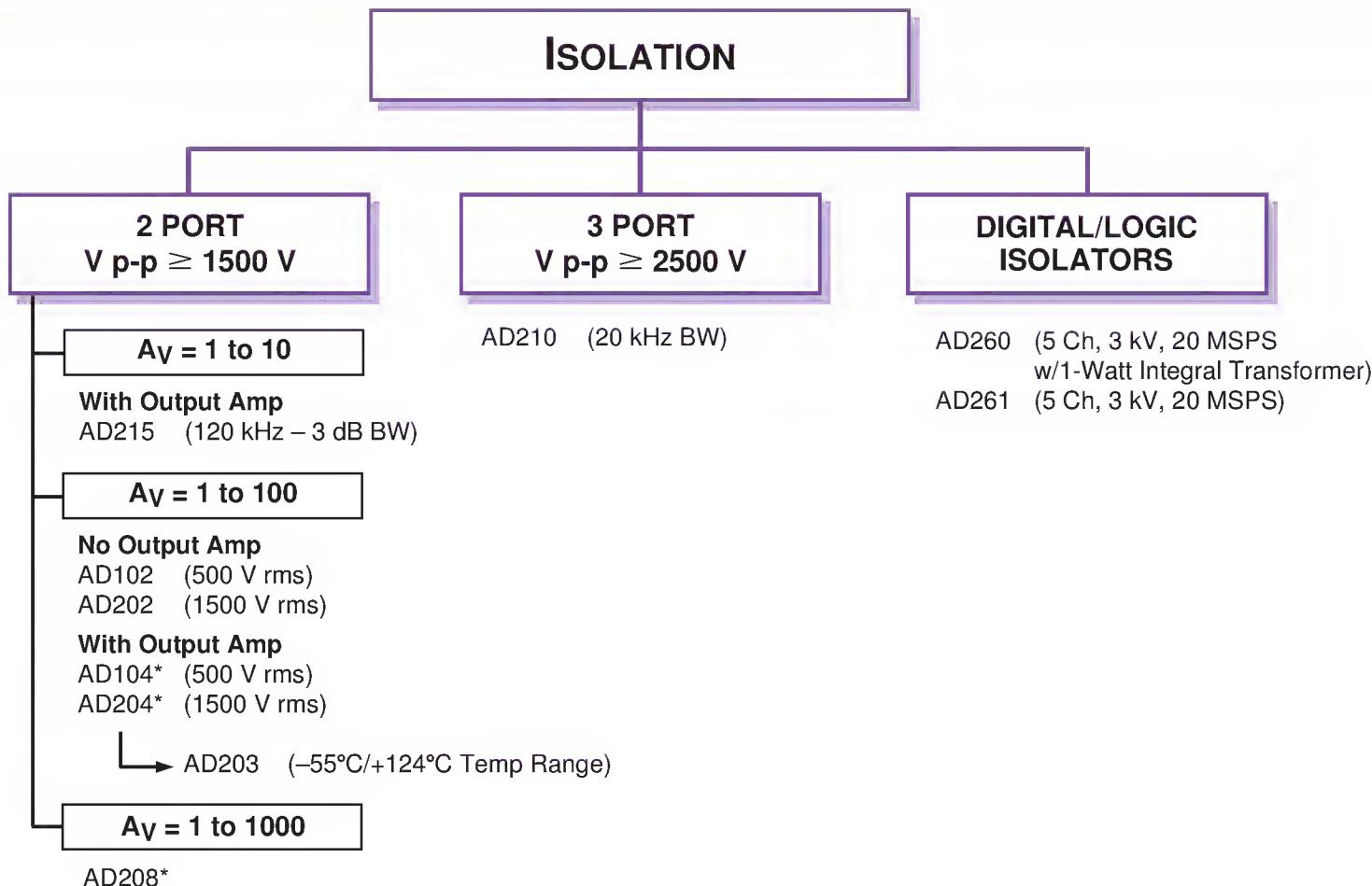
AMPLIFIERS



AMPLIFIERS



AMPLIFIERS



*Multichannel Requires Clock Driver AD246

AMPLIFIERS

LOG AMPS

- AD606 (50 MHz, 80 dB, Limiter)
- AD640 (120 MHz, 50 dB)
- AD8307 (500 MHz, 86 dB)
- AD641 (250 MHz, 44 dB)

VARIABLE GAIN

ANALOG CONTROL, AUDIO

- 140 dB Gain Range
 - SSM2018T (Voltage Output)
 - SSM2018T (Current Output)
- 120 dB Gain Range
 - SSM2164 (Quad)

DIGITAL CONTROL, VIDEO

- +5 V Supply
 - AD8320 (8-Bit, 200 MHz, 36 dB)

ANALOG CONTROL, VIDEO

±15 V Supplies

- AD600 (35 MHz, Dual, 0 to +40 dB)
- AD602 (35 MHz, Dual, -10 to +30 dB)
- AD603 (115 MHz, -10 to +30 dB)
- AD604 (50 MHz, Dual, w/Pre/Amp)

+5 V Single Supply

- AD605 (45 MHz, +48 dB/Channel)

LCD DRIVERS

+3.3 V, 2 Muxed Input Channels/Drivers

- AD8509 (9 Drivers, I_{OUT} = 20 mA/Ch)
- AD8511 (11 Drivers, I_{OUT} = 20 mA/Ch)

ANALOG COMPUTATIONAL CIRCUITS

MULTIPLIERS/ DIVIDERS/ SQUARE ROOT

Voltage Output

AD534 (1 MHz, Precision)
AD633 (1 MHz, Low Cost)
AD734 (10 MHz)
AD835 (250 MHz)
MLT04 (8 MHz, Quad)

Current Output

AD539 (60 MHz)
AD834 (500 MHz)

Special Function

AD538 ($V_O = V_Y \left(\frac{V_Z}{V_X} \right)^M$ M = 1/5 to 5)

RMS CONVERTERS

$\pm 15V$ Rails

AD536A (2.3 MHz, BW @ 1V)
AD637 (8.0 MHz, BW @ 1 V)

Low Power $\pm 5V$ Rails

AD737 (0.16 mA)
AD736 (0.2 mA)
AD636 (1.0 mA)

TRIG FUNCTION GENERATORS

AD639

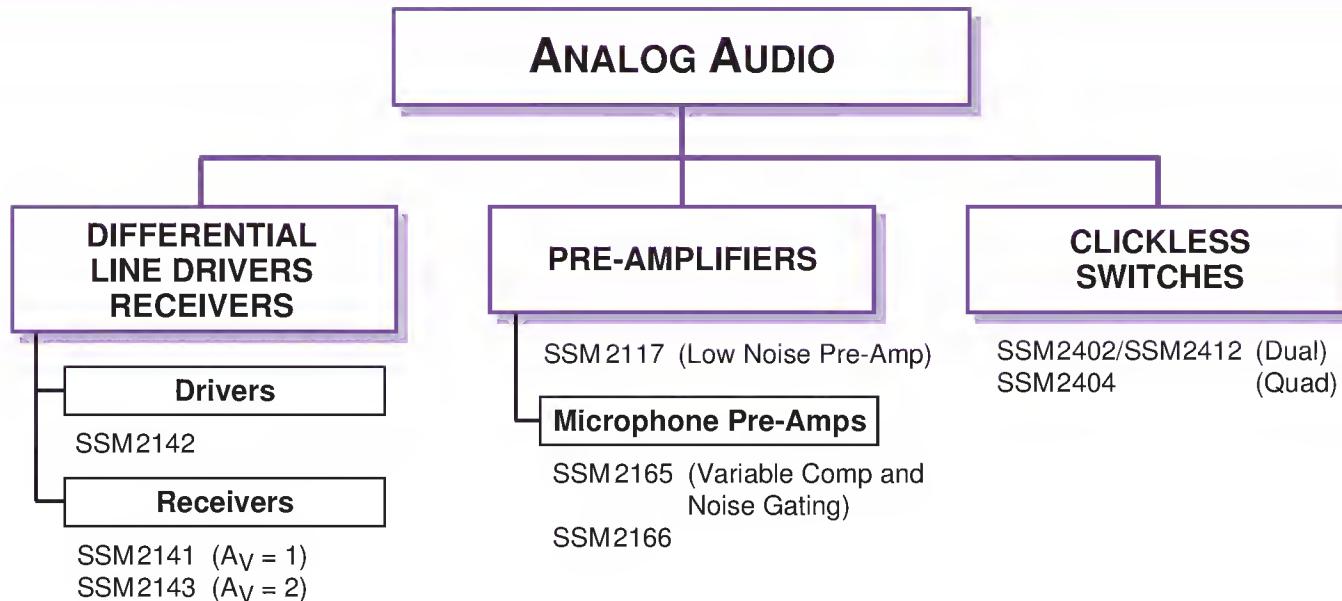
BALANCED MODULATORS

AD630 (2 MHz)

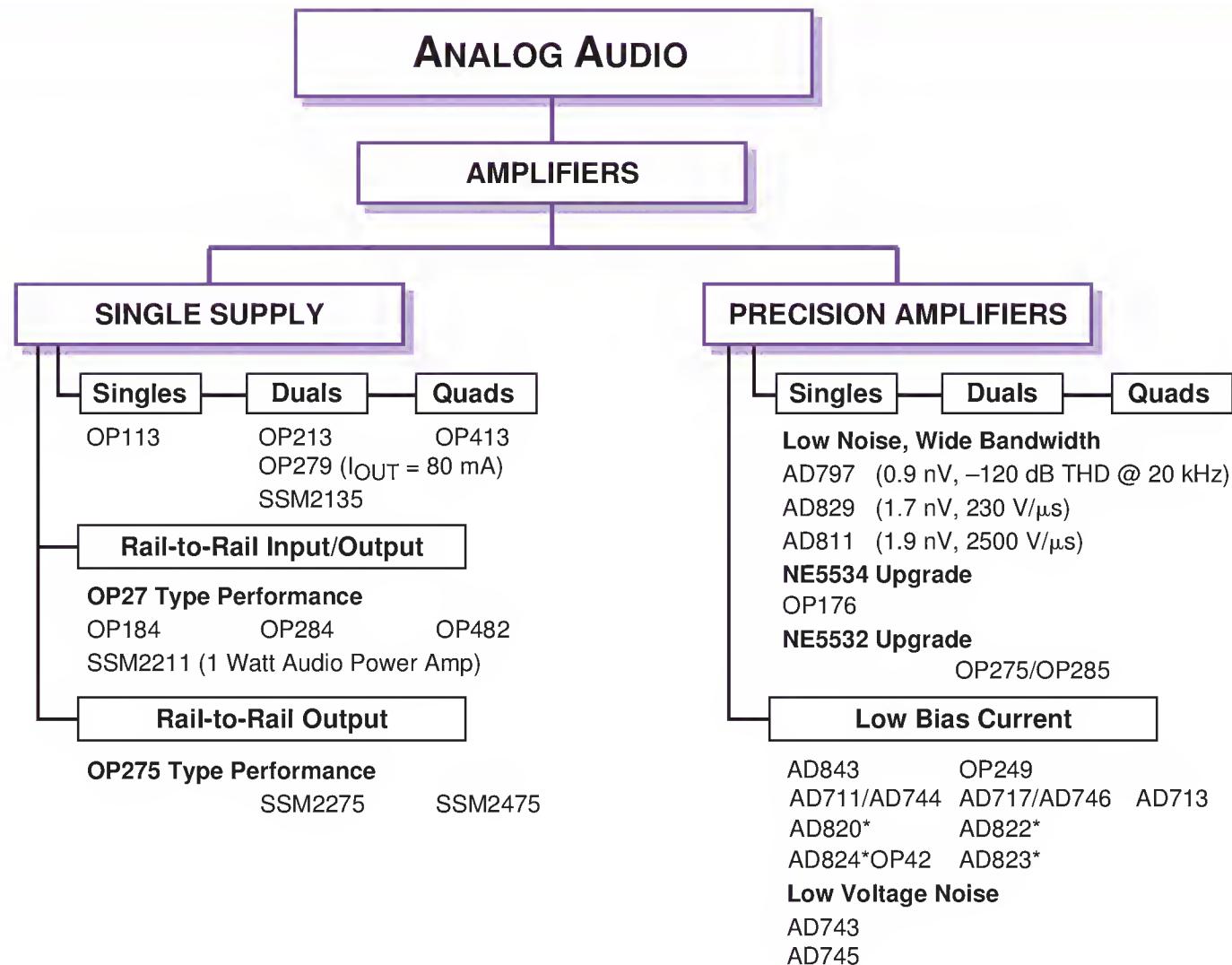
RGB TO NTSC/PAL

See Video Section.

ANALOG/DIGITAL AUDIO

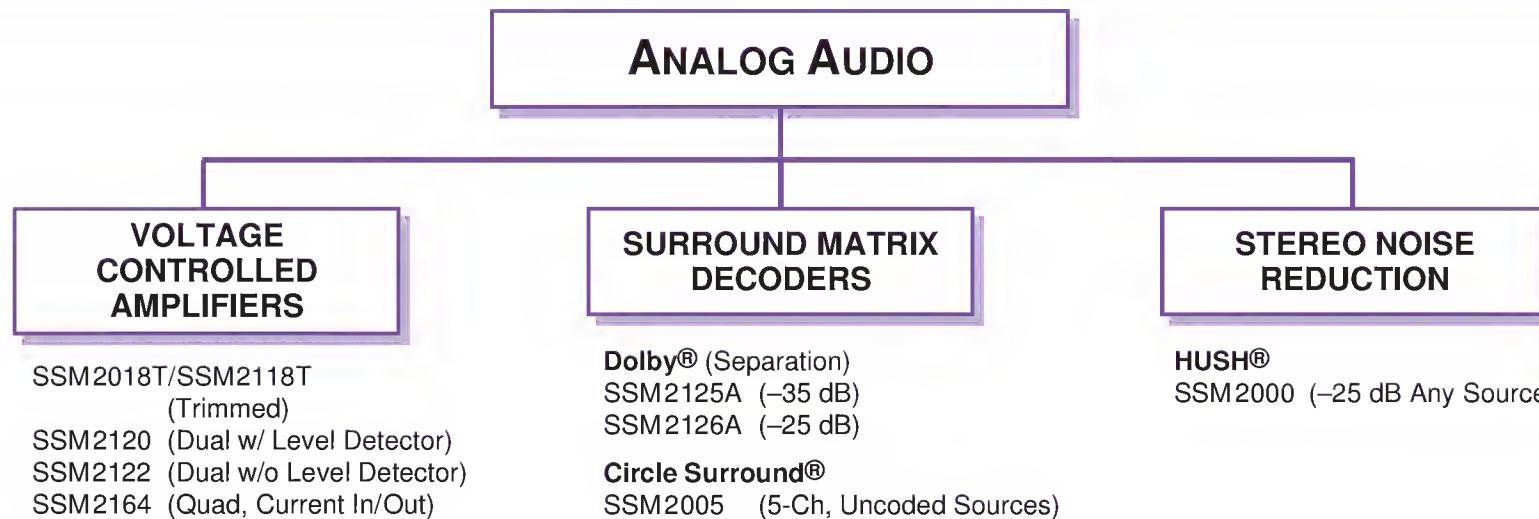


ANALOG/DIGITAL AUDIO



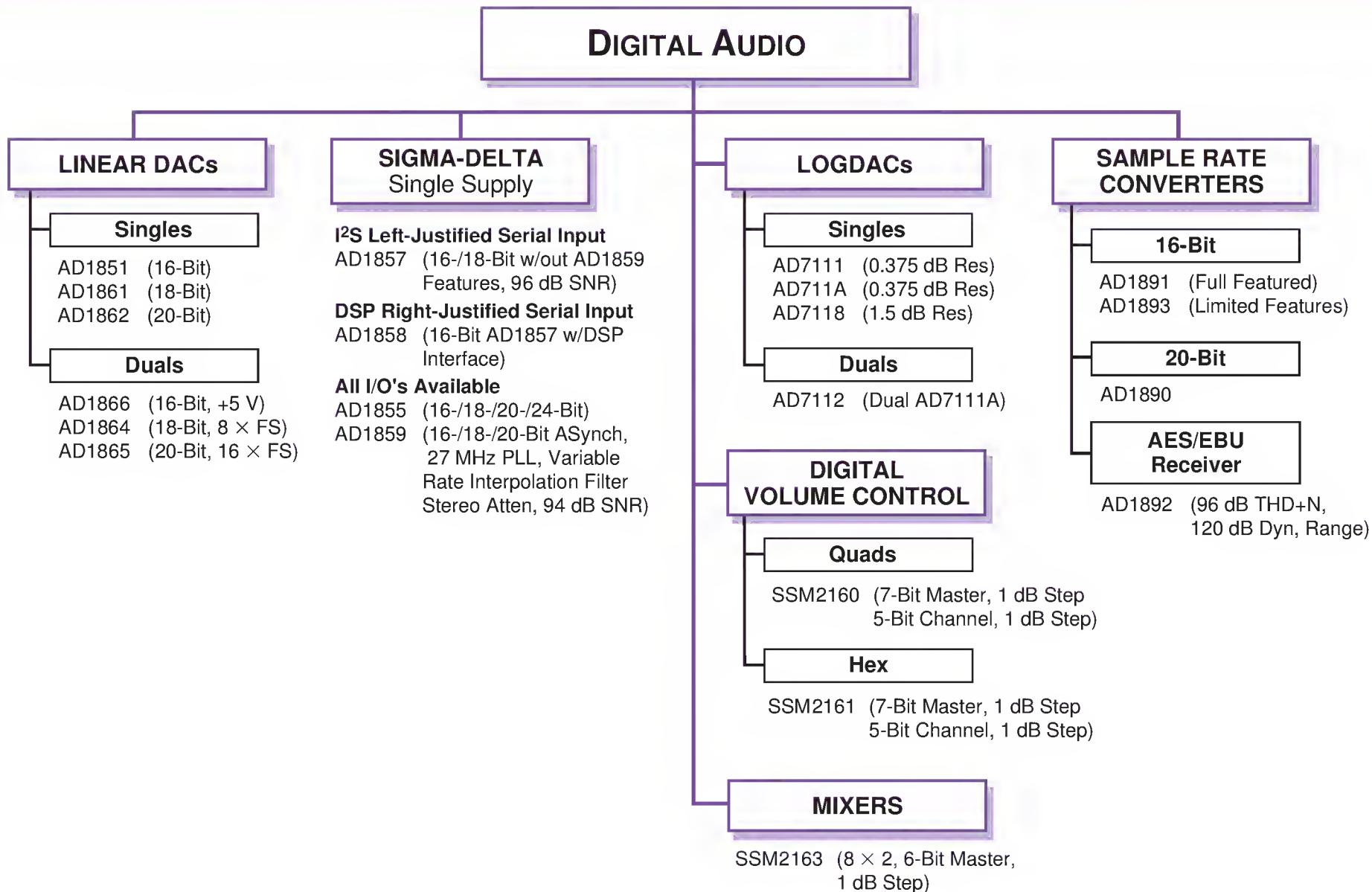
*Rail-to-Rail Output.

ANALOG/DIGITAL AUDIO

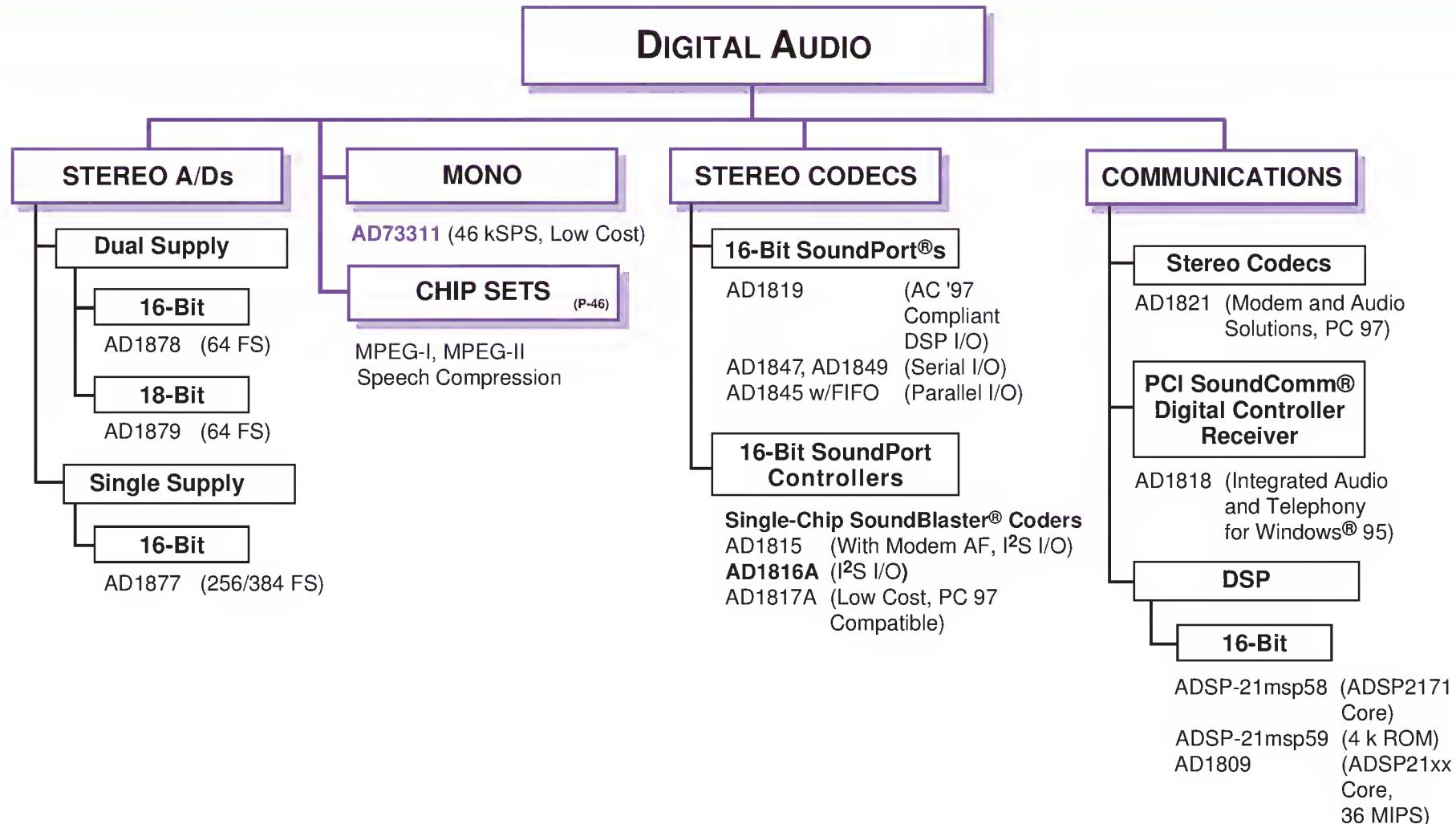


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Circle Surround is a Registered Trademark of Rocktron Corp.
HUSH is a Registered Trademark of Hush Systems.

ANALOG/DIGITAL AUDIO

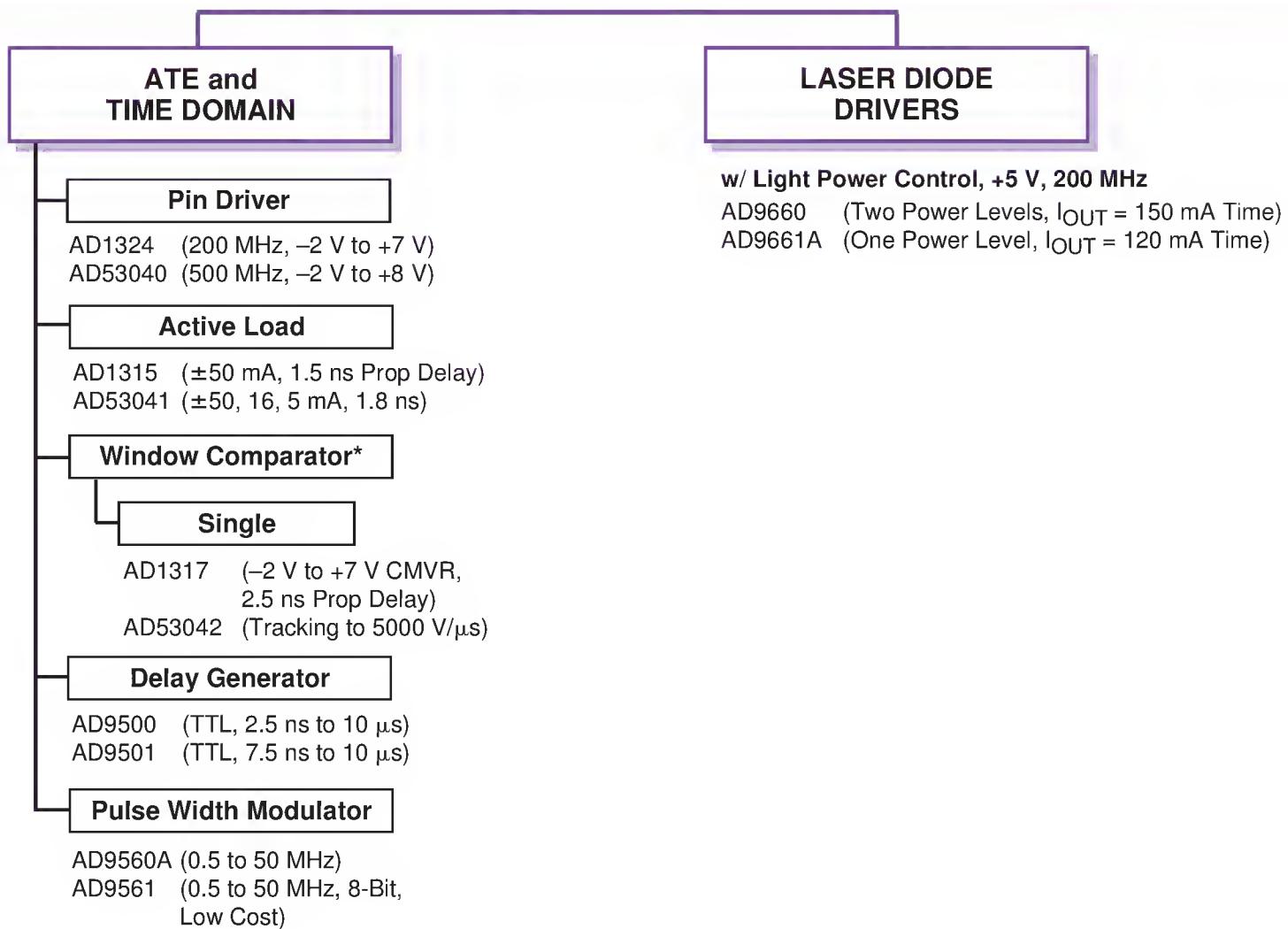


ANALOG/DIGITAL AUDIO



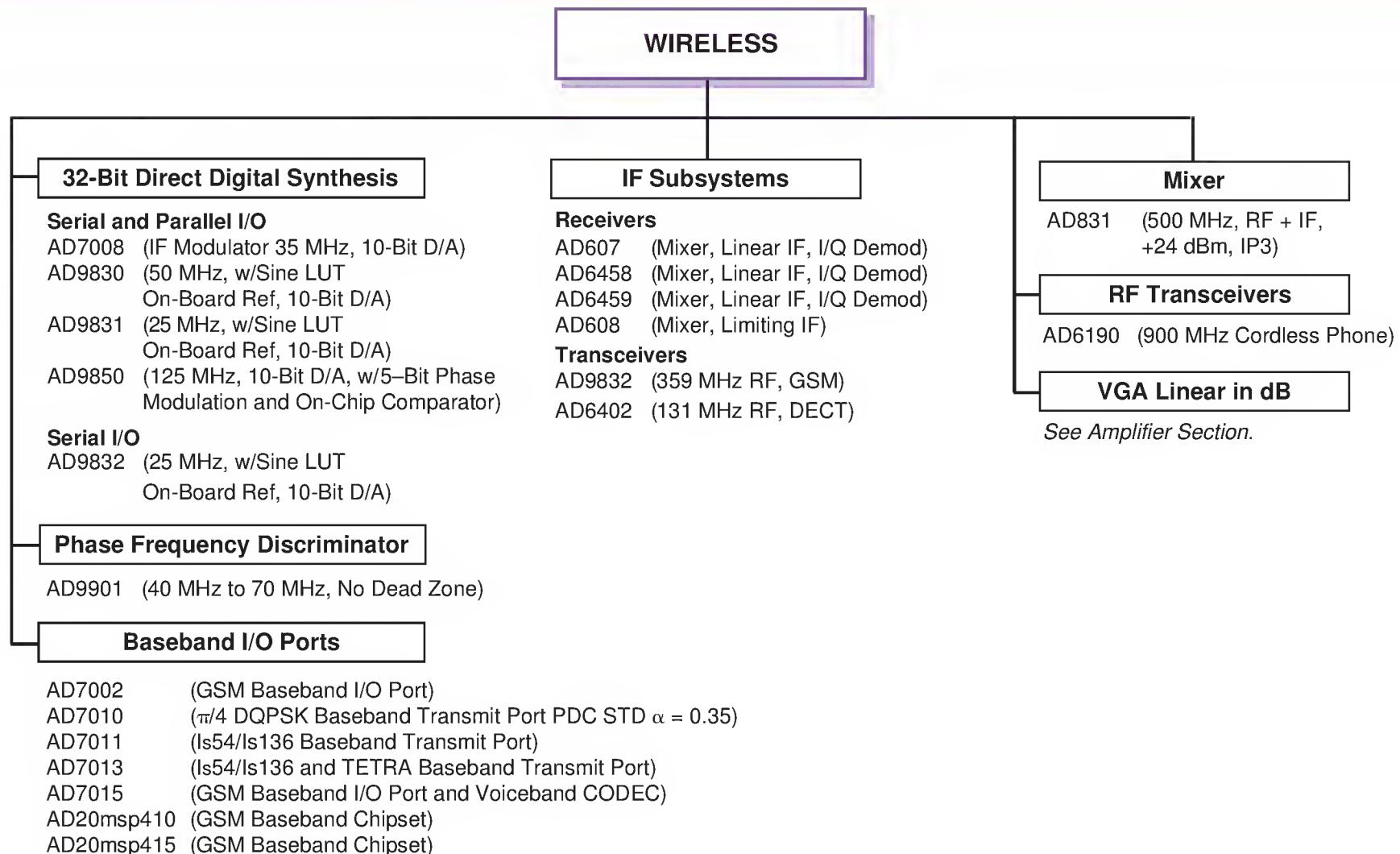
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ATE, TIME DOMAIN & LASER DIODE DRIVERS



*For Precision Comparators See Page 33.

COMMUNICATIONS



See Amplifier Section.

COMMUNICATIONS

TELECOM/DATACOM

Combo Modem/Voice/Fax

AD1821 (PC 97 Logo Compatible)

DSP Processors

ADSP-21msp58 (ADSP-2171 and Voiceband Codec)

ADSP-21msp59 (AD21msp58 w/ROM)
(ADSP-21xx + Modem/Voice/Fax Controller)

AD1809 (Host-Based Modem)

Multimedia Cable Networking Systems

AD6201 (Complete Single Chip Solution)

CHIPSETS

GSM Baseband

AD20msp410 (GSM Baseband Chipset)

Diversity Base Station Receiver

AD6600/AD6620 (65 MSPS Dual A/D)

IS-54/IS-136

AD607, AD7011, AD7013

Multichannel/Mode Base Station Receiver

65 MSPS Decimating Receiver

AD6640/AD6620 (12-Bit, 65 MSPS A/D)

DECT

Baseband Radio Circuitry

AD6600

ADSL

AD20msp910 Chipset

AD6435 (Digital Interface Control IC)

AD6436 (DMT Coprocessor)

AD6437 (Analog Front End)

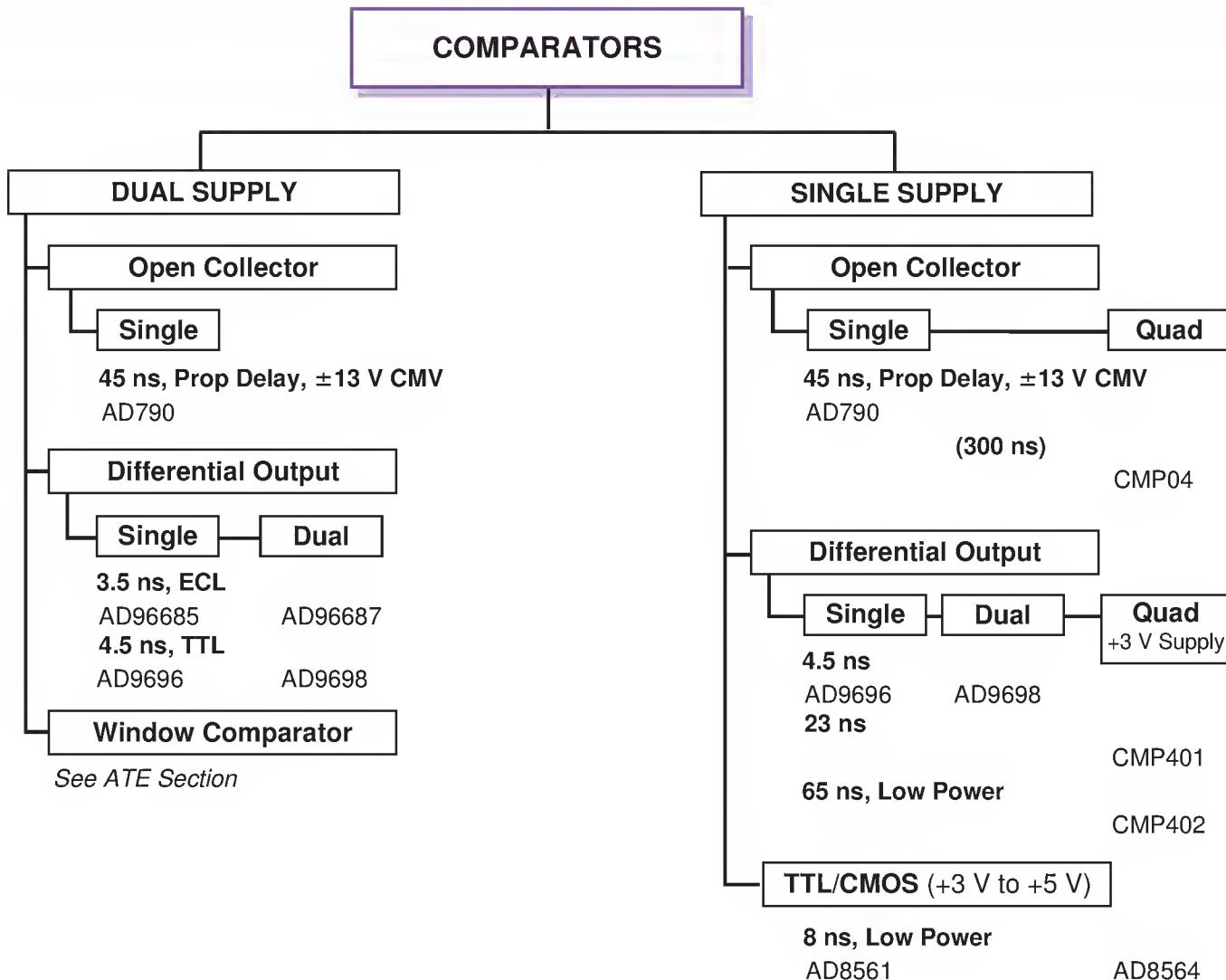
GPS

ADSST-NAV2000 (8-Ch, SHARC®-Based)

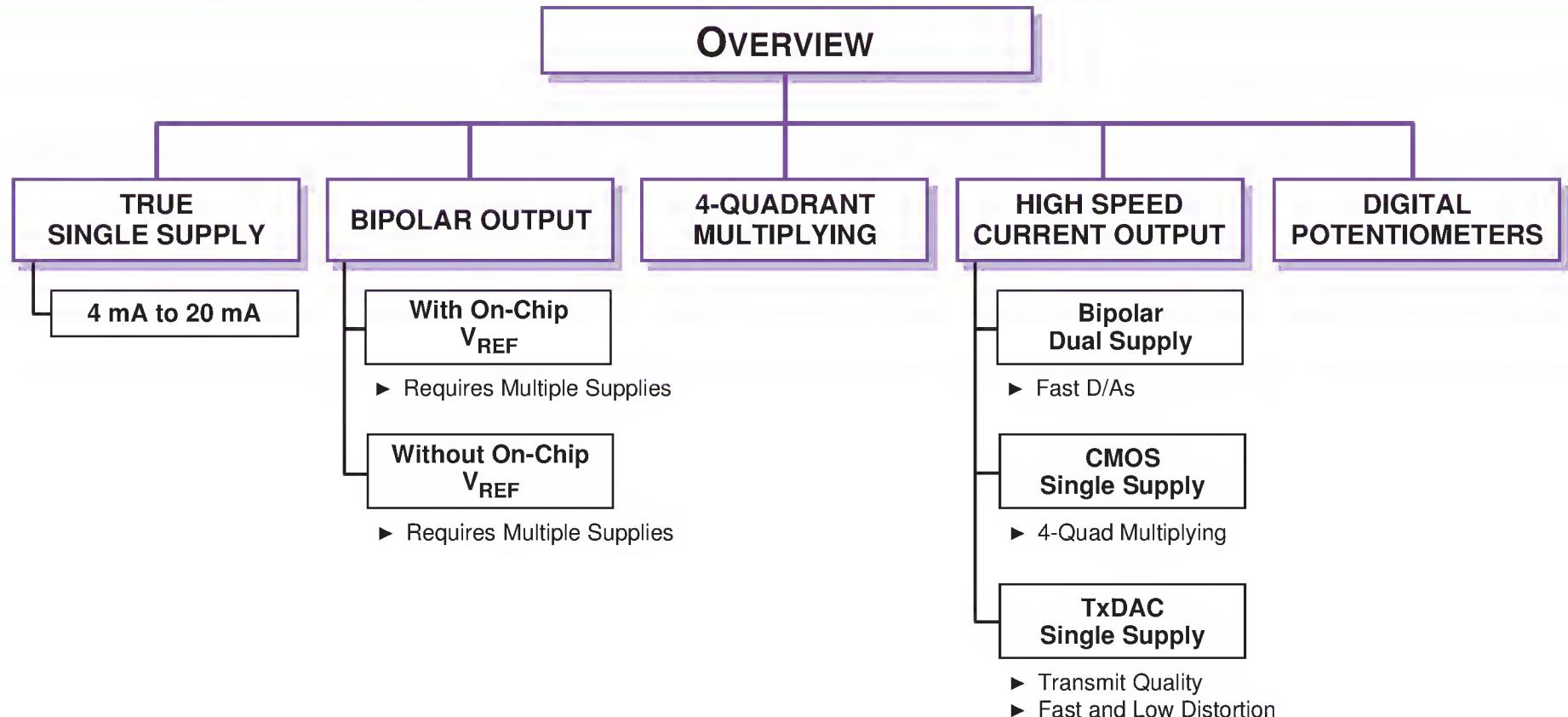
ADSST-NAV2100 (8-Ch, Fixed-Point-Based)

SHARC is a Registered Trademark of Analog Devices, Inc.

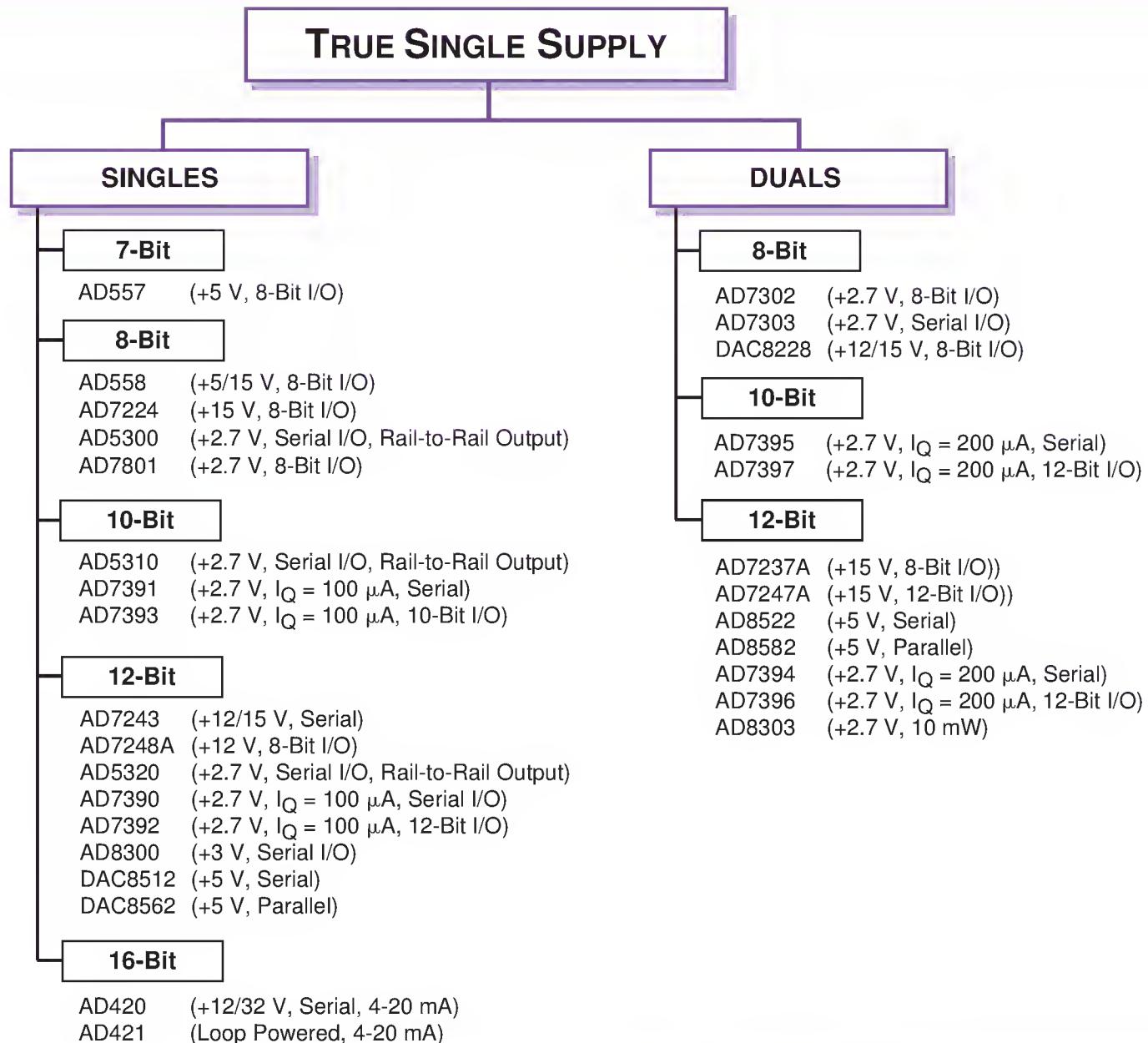
COMPARATORS



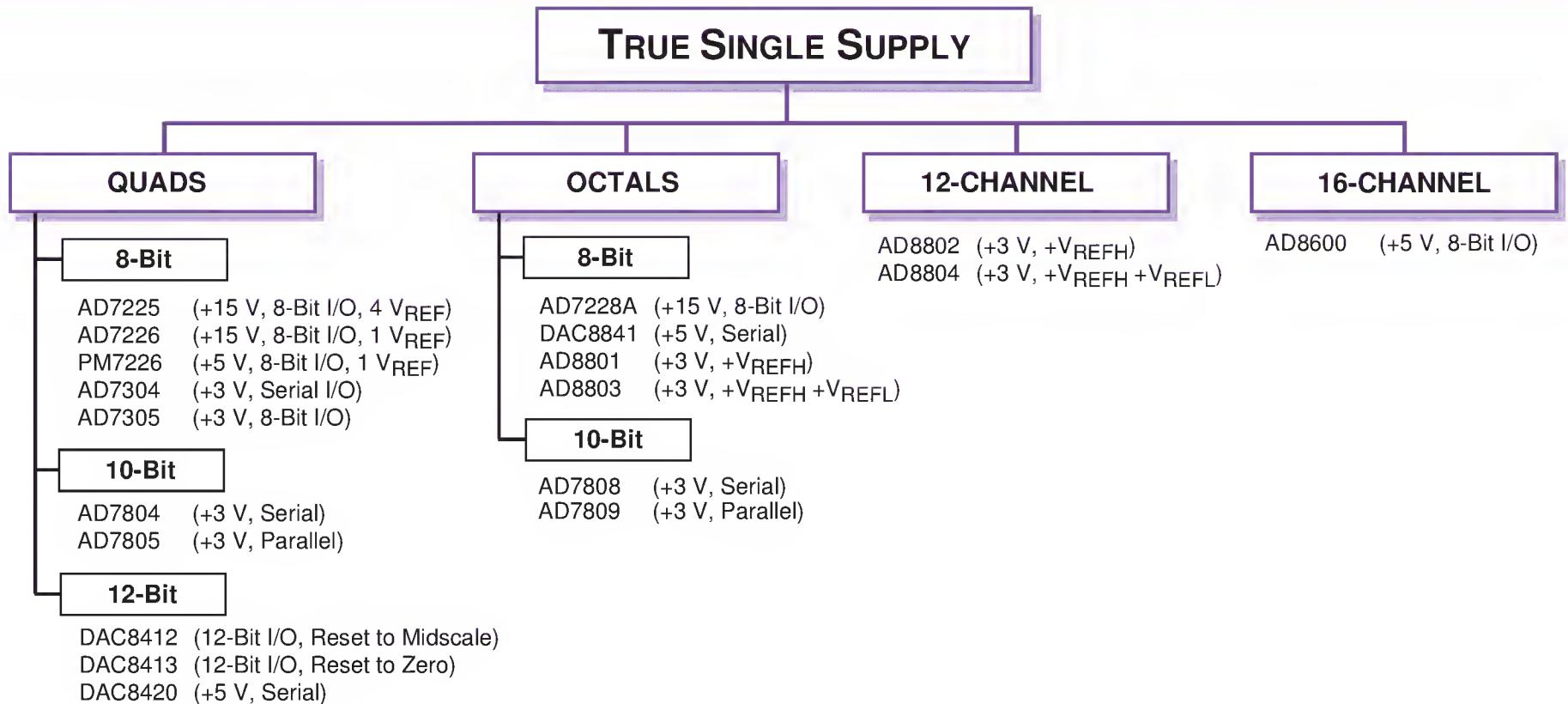
D/A CONVERTERS



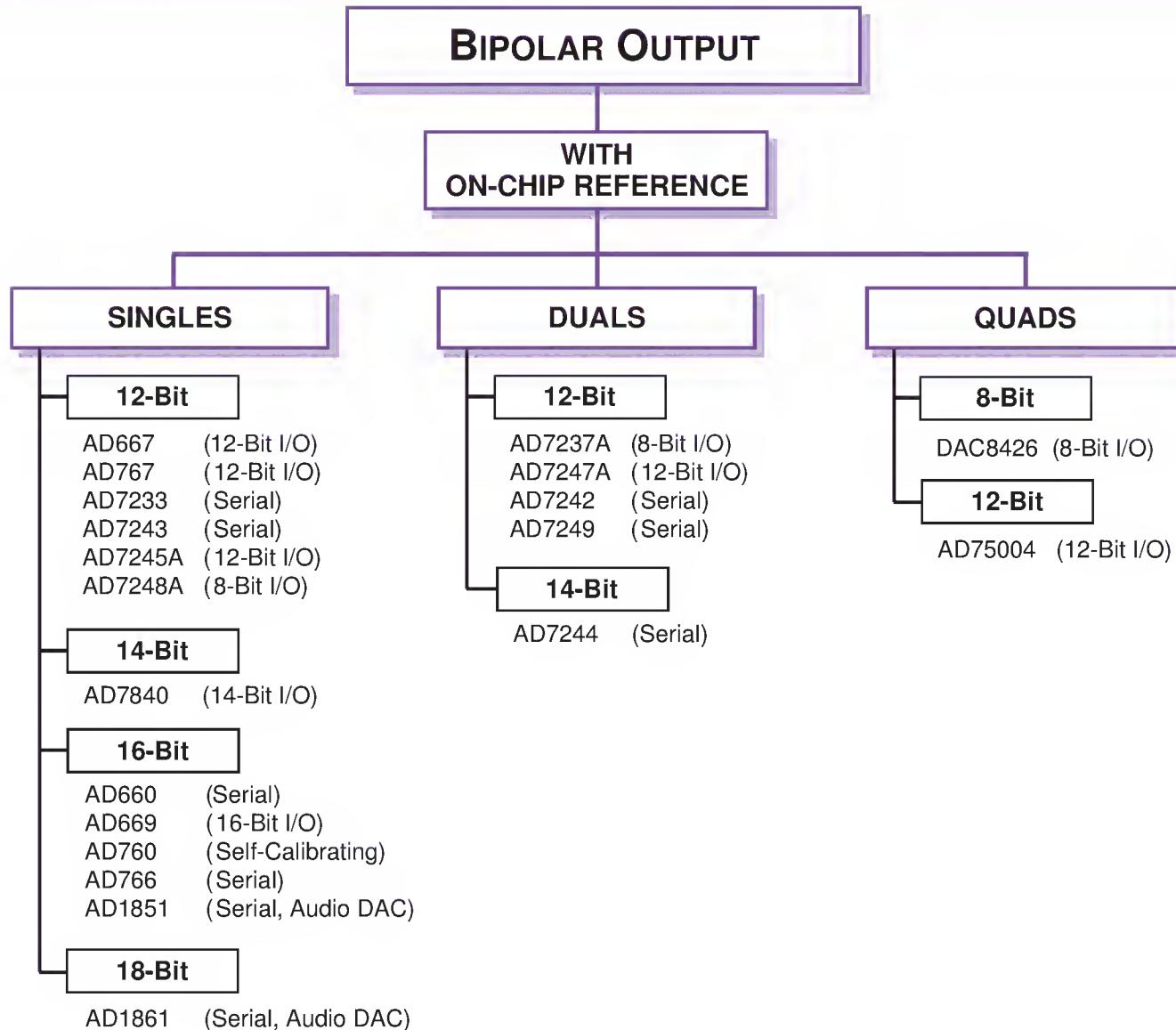
D/A CONVERTERS



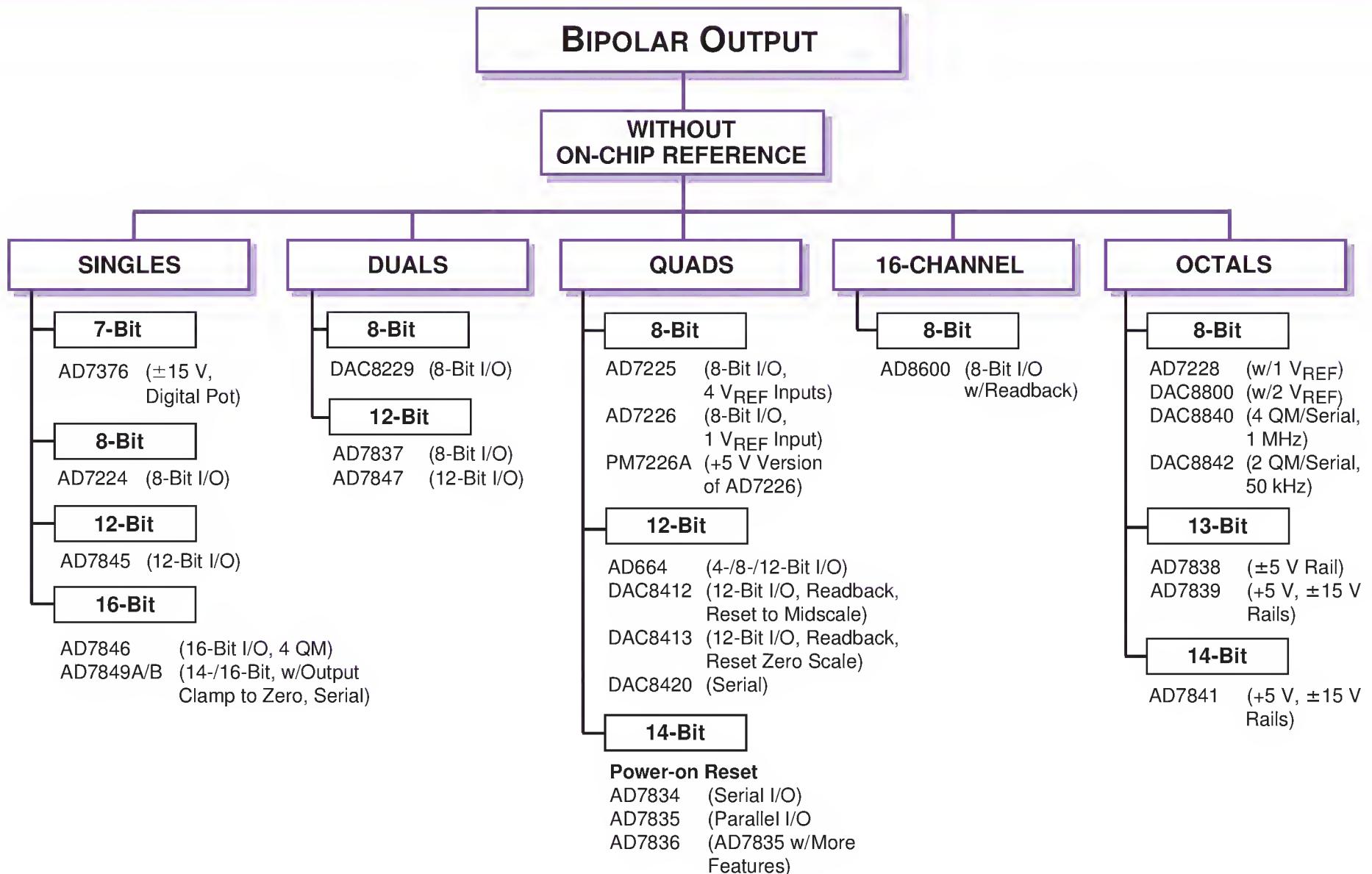
D/A CONVERTERS



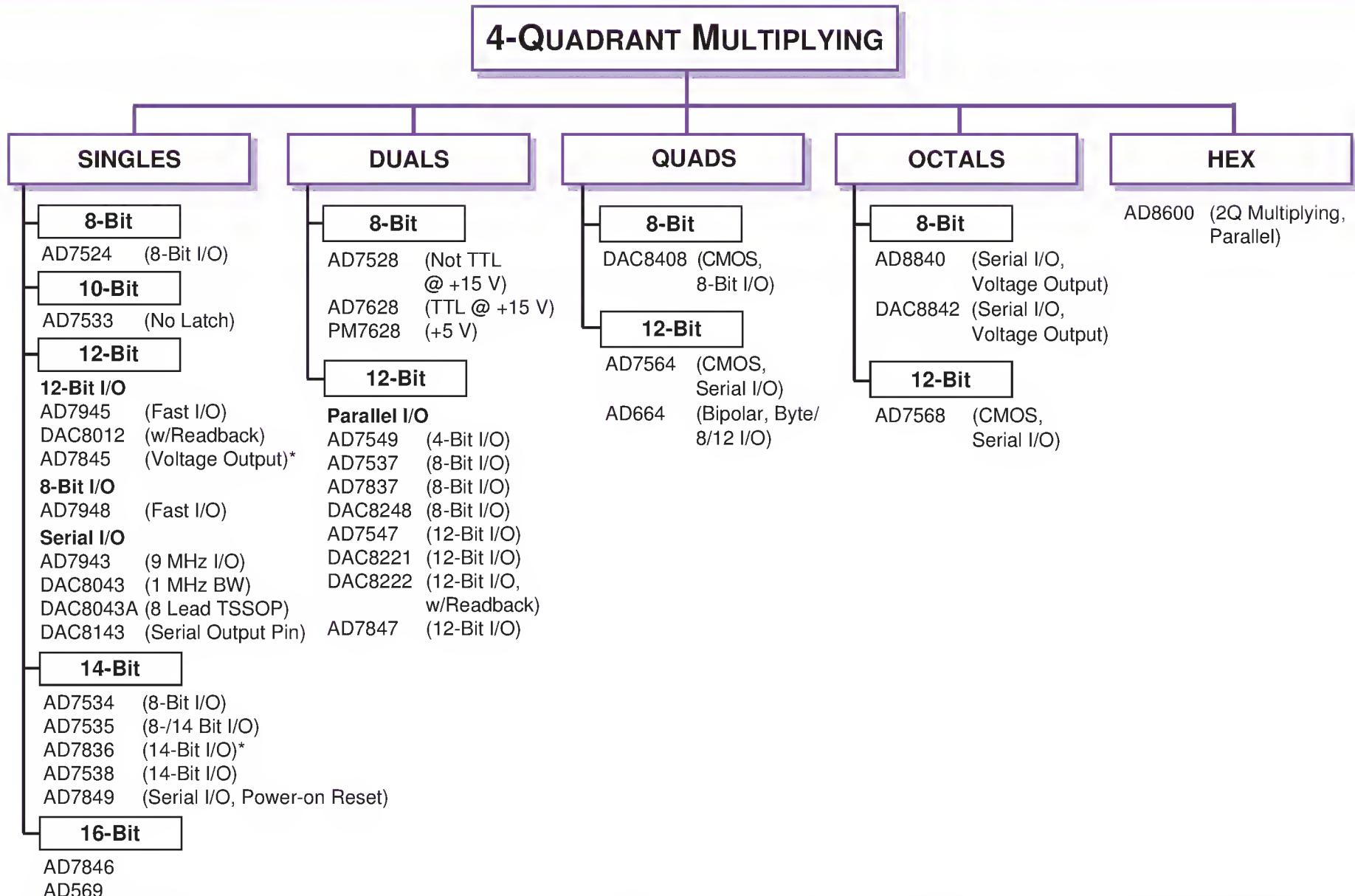
D/A CONVERTERS



D/A CONVERTERS

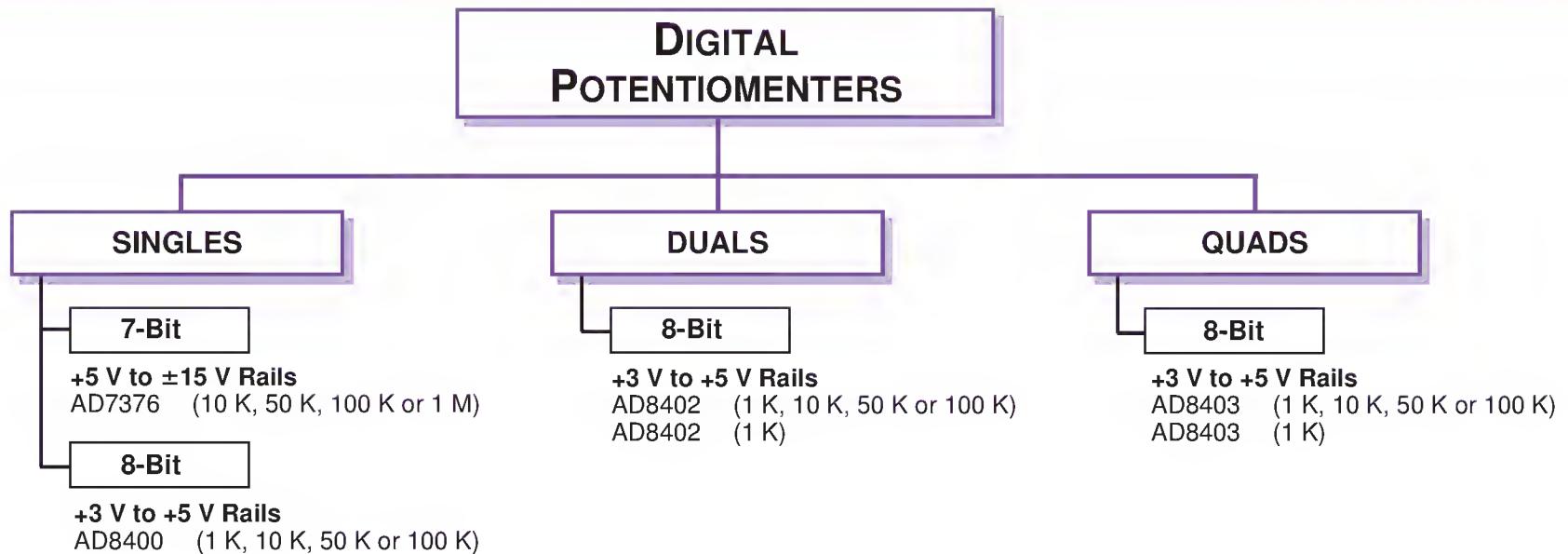


D/A CONVERTERS



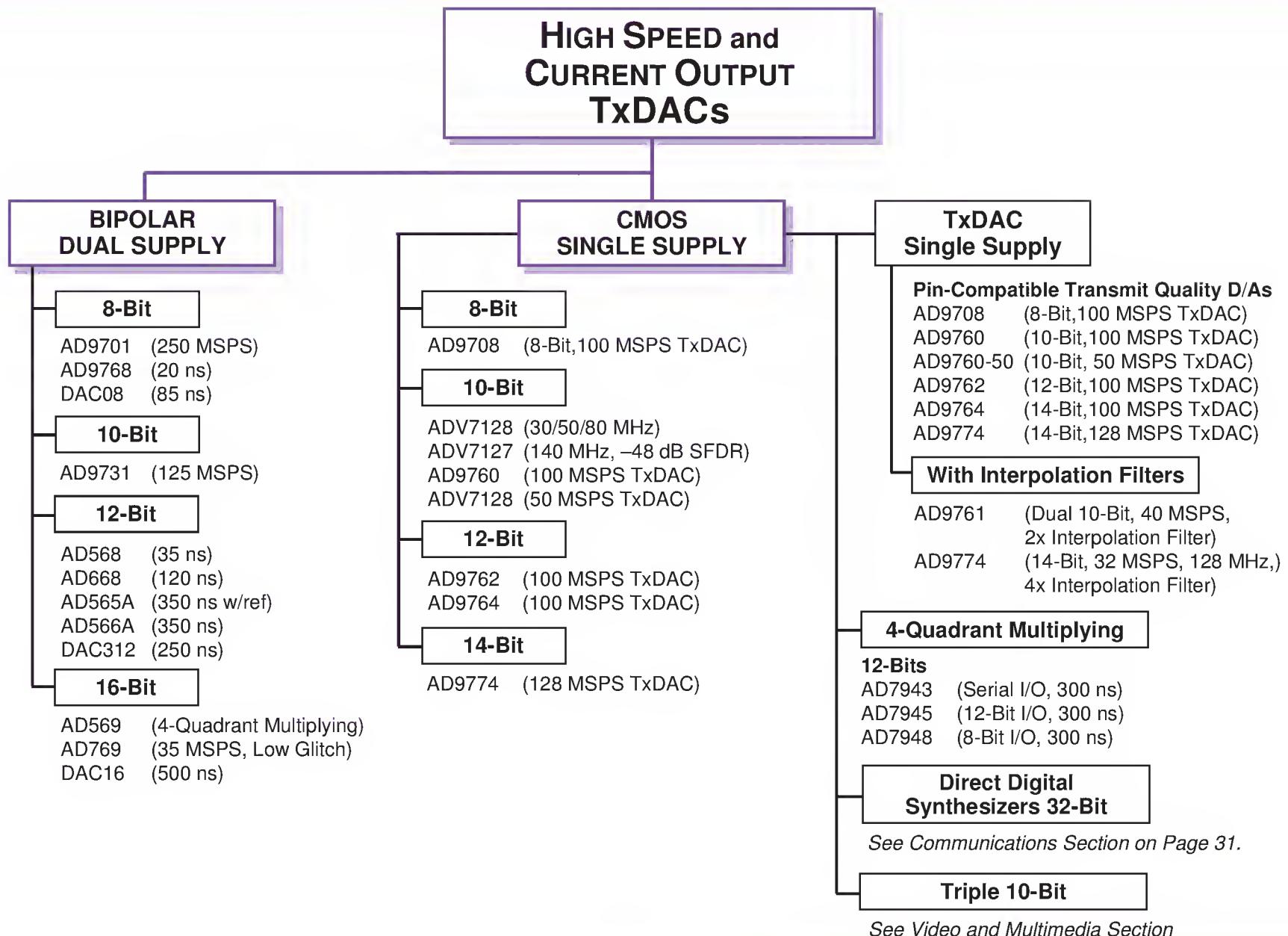
*Voltage switching ladder.

D/A CONVERTERS

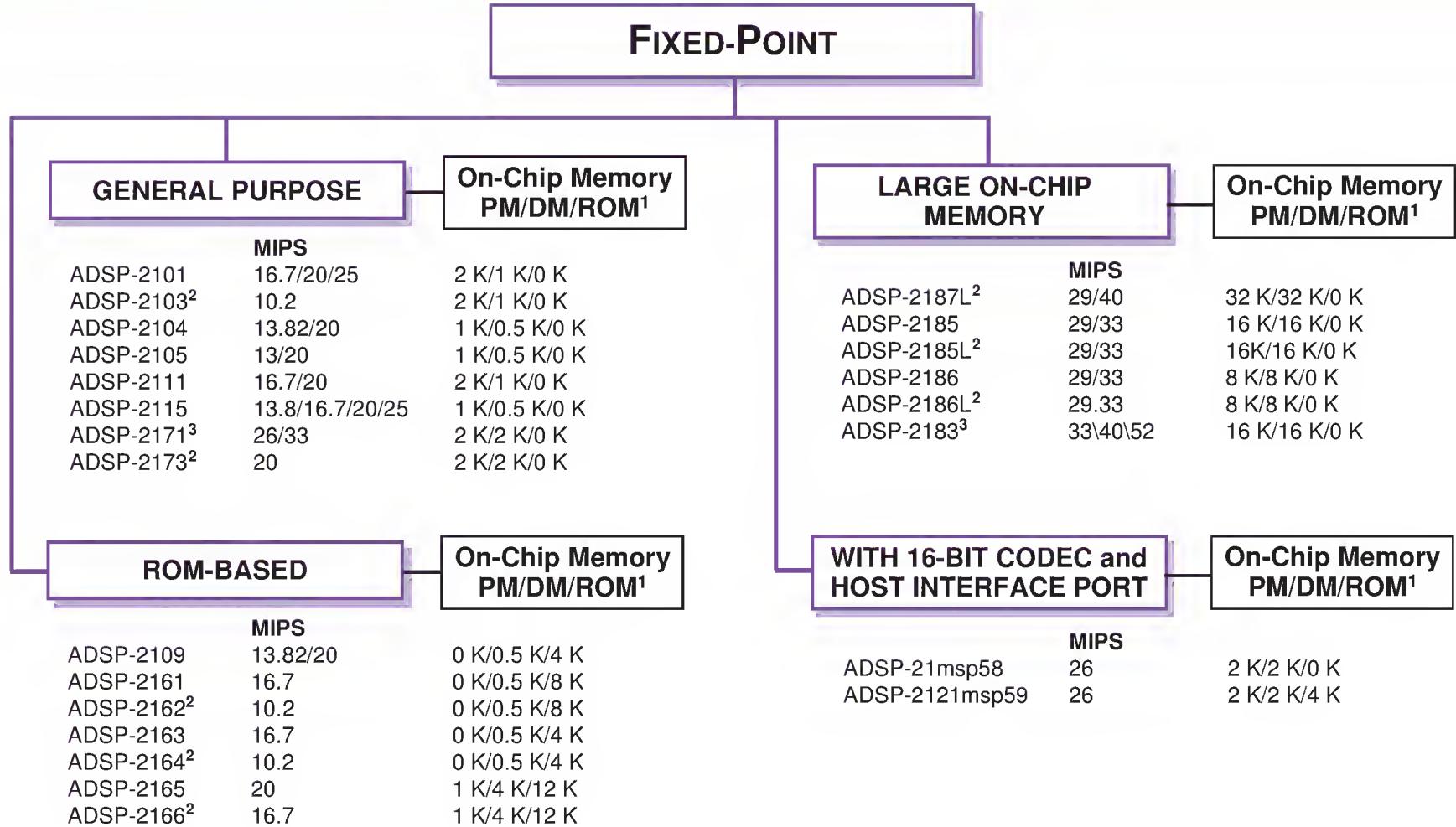


*All Have Serial I/O

D/A CONVERTERS



DIGITAL SIGNAL PROCESSING



¹ PM/DM/ROM = Program RAM/Data RAM/Program ROM

² +3.3 V Operation

³ Multiple Power-Down Modes

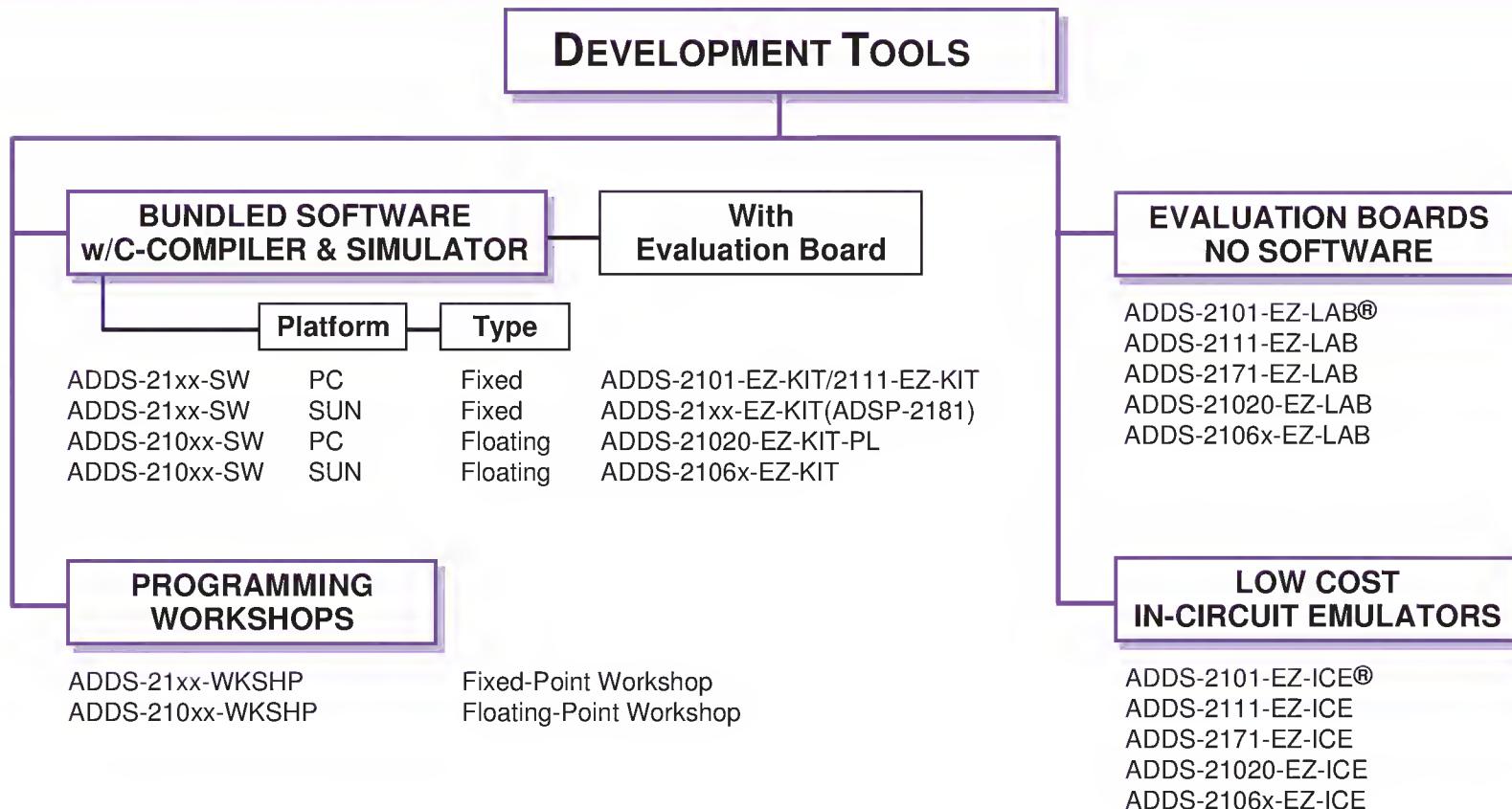
DIGITAL SIGNAL PROCESSING

FLOATING-POINT

| Cycle Time ns | | On-Chip Memory PM/DM ¹ |
|--------------------|------------|---|
| ADSP-21020 | 25/40/50 | 0 K/0 K |
| ADSP-21060 | 25 | 2 Mbit/2 Mbit |
| ADSP-21061 | 25 | 0.5 Mbit/0.5 Mbit |
| ADSP-21061L | 25 | 3 V Operation |
| ADSP-21062 | 25 | 1 Mbit/1 Mbit |
| ADSP-21062L | 25 | 3 V Operation |
| AD14060 | 25 | 8 Mbit/8 Mbit |
| QUAD ADSP-21060 | | On-Chip Memory PM/DM ¹ |
| AD14160 | 480 MFlops | 8 Mbit/8 Mbit |
| AD14160L | 480 MFlops | 8 Mbit/8 Mbit, 3 V Operational |
| AD14061 | 480 MFlops | 8 Mbit/8 Mbit, 452 Lead Ball Grid Array |
| AD14061L | 480 MFlops | 8 Mbit/8 Mbit, 452 Lead Ball Grid Array. +3 V |

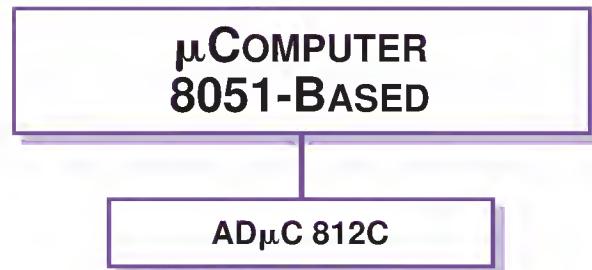
¹PM/DM = Program RAM/Data RAM

DIGITAL SIGNAL PROCESSING



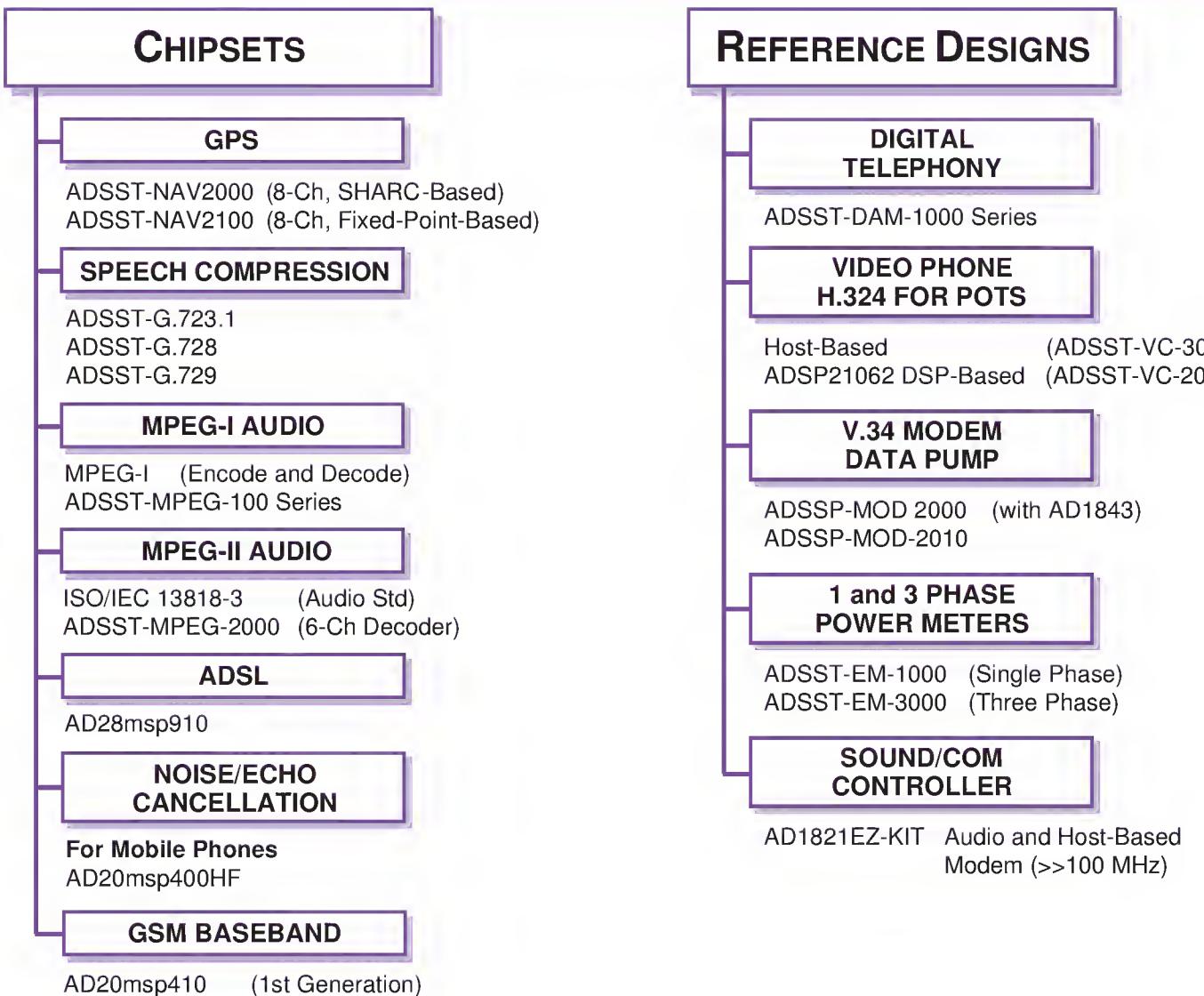
ADSP-21csp01: Compilers/Software/Evaluation Boards and Emulators Are Under Development.
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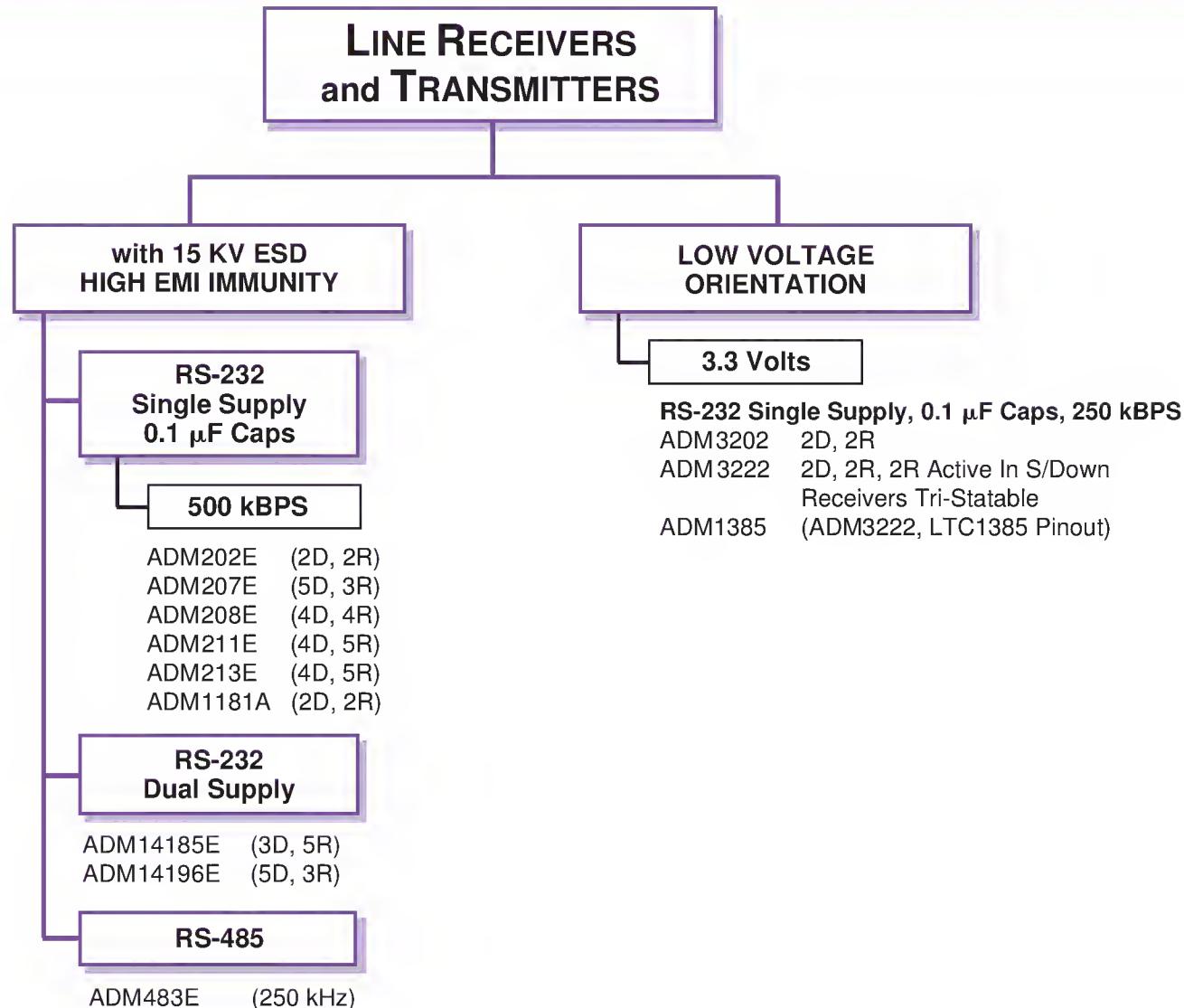
DIGITAL SIGNAL PROCESSING



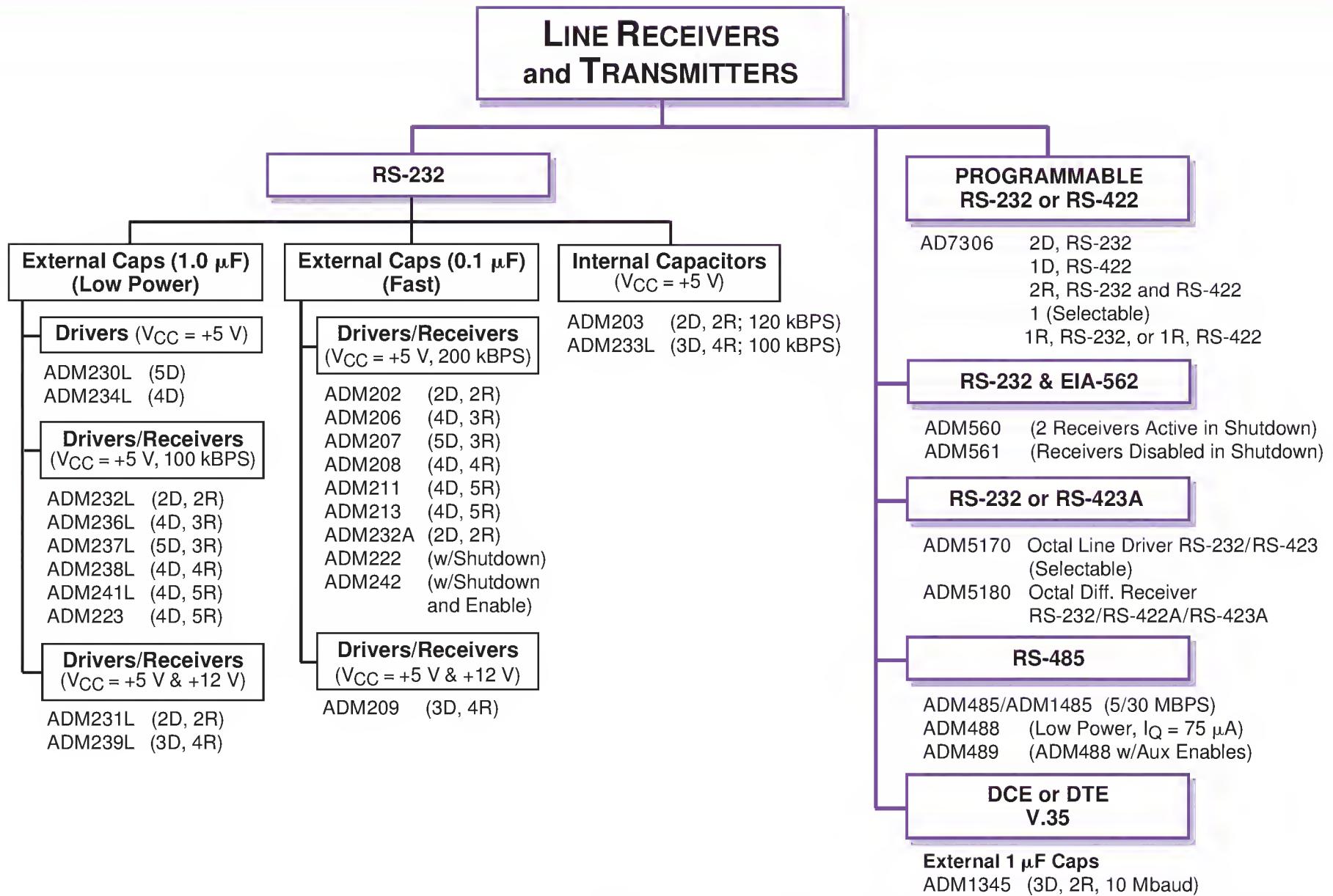
- ▶ 16 MHz Operation
- Analog I/O
- 8-Ch, 200 kSPS, 12-Bit A/D

DIGITAL SIGNAL PROCESSING

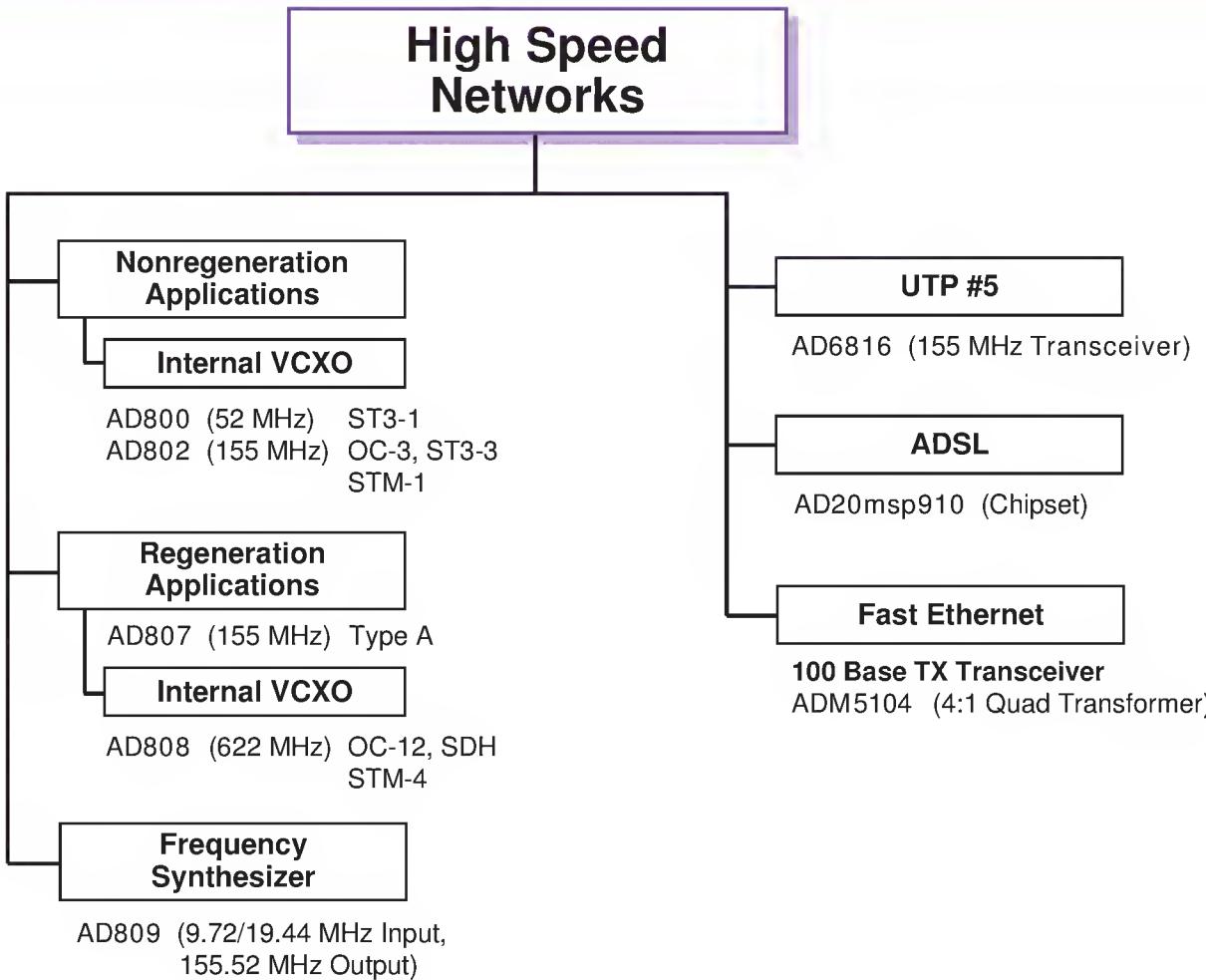




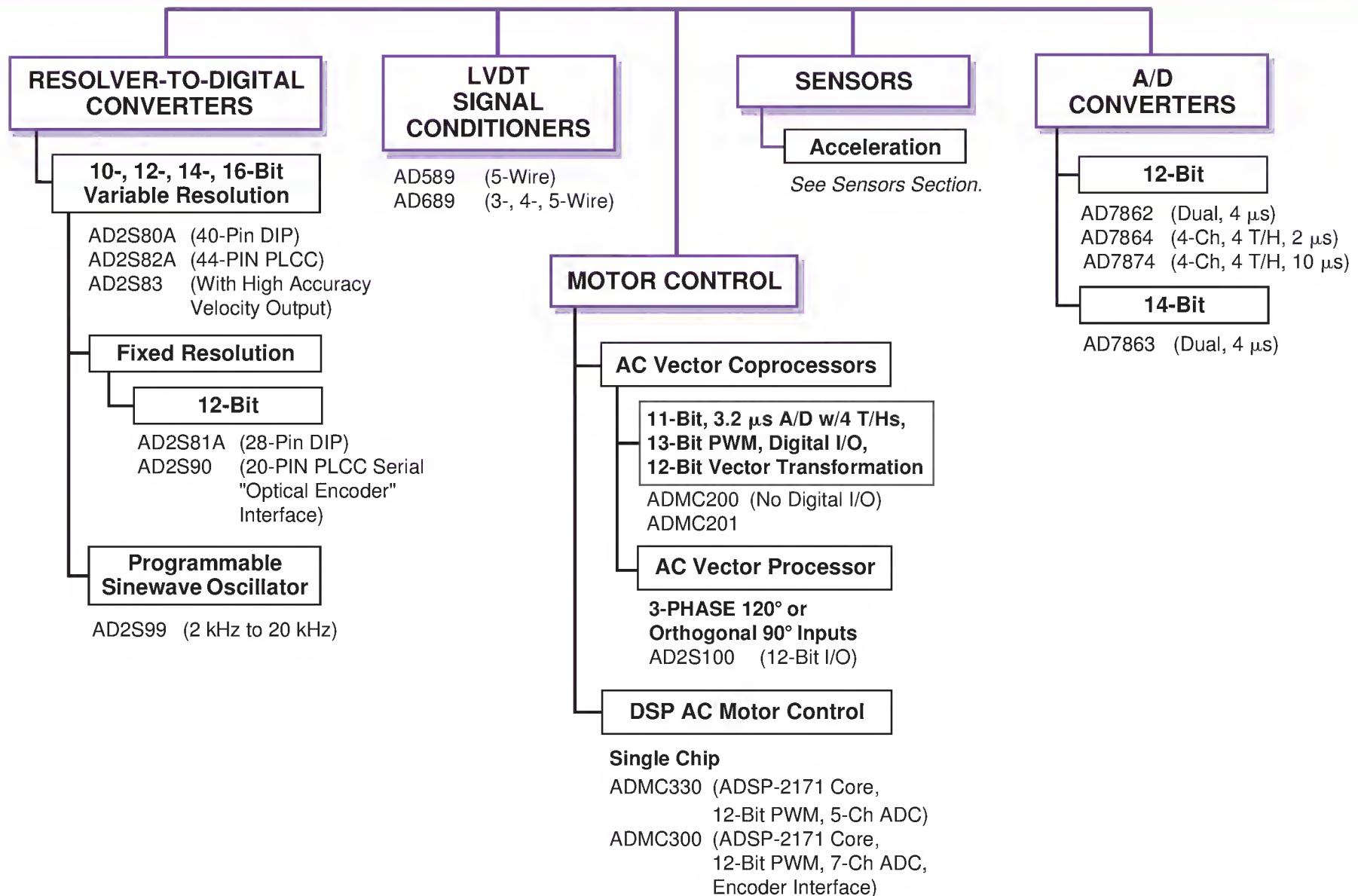
INTERFACE



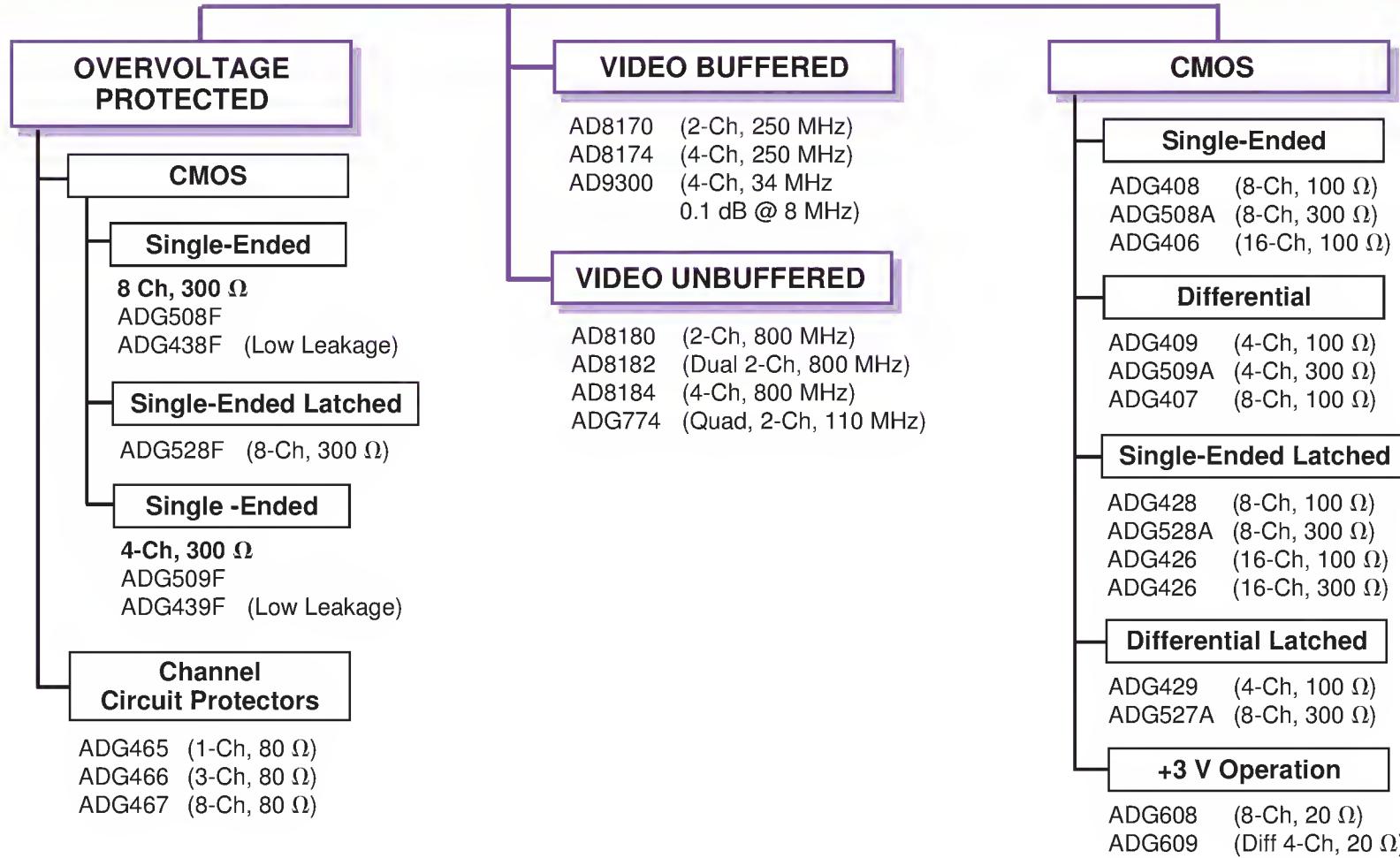
LANS: LOCAL/WIDE AREA NETWORKS



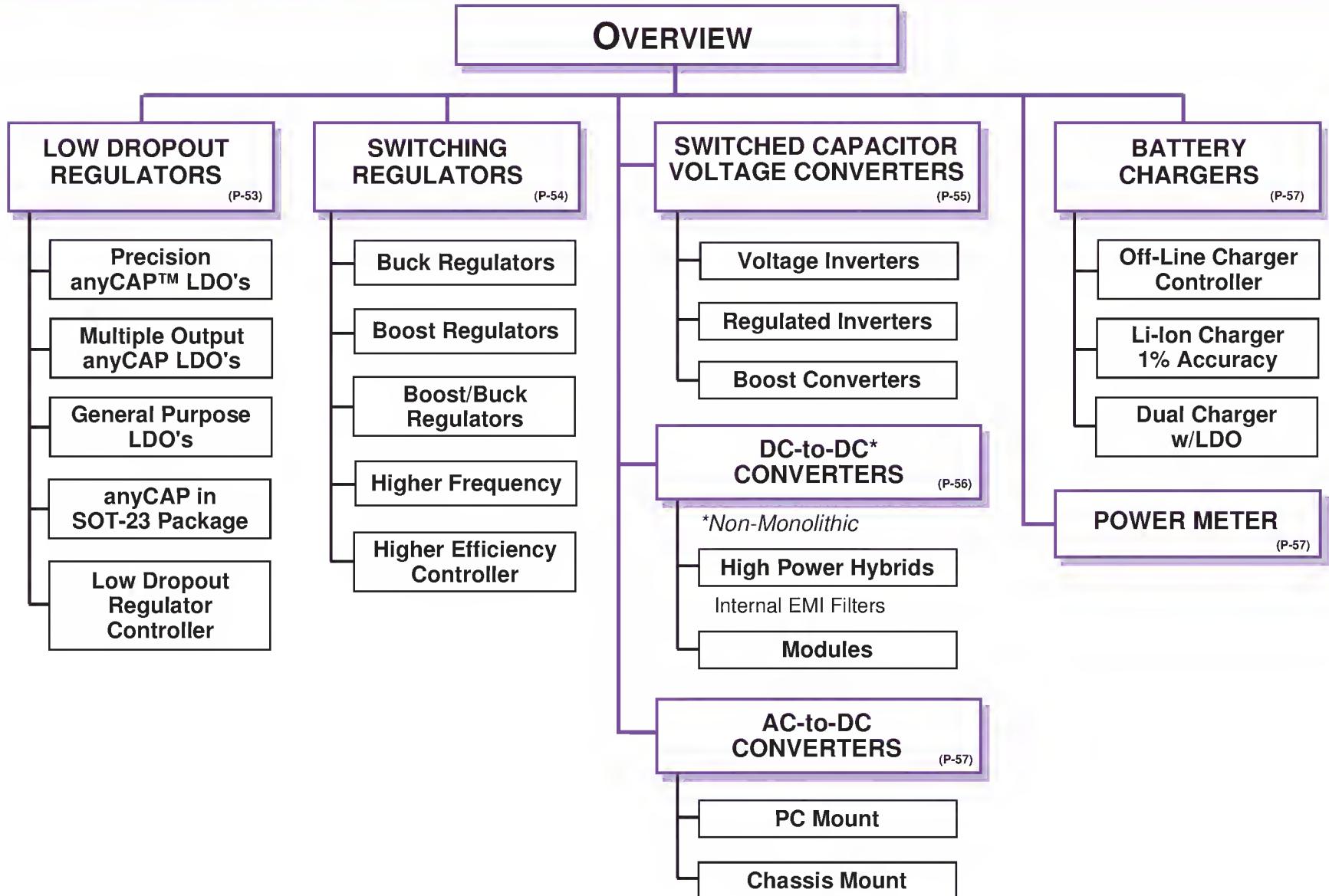
MOTION CONTROL INTERFACE



MULTIPLEXERS



POWER MANAGEMENT



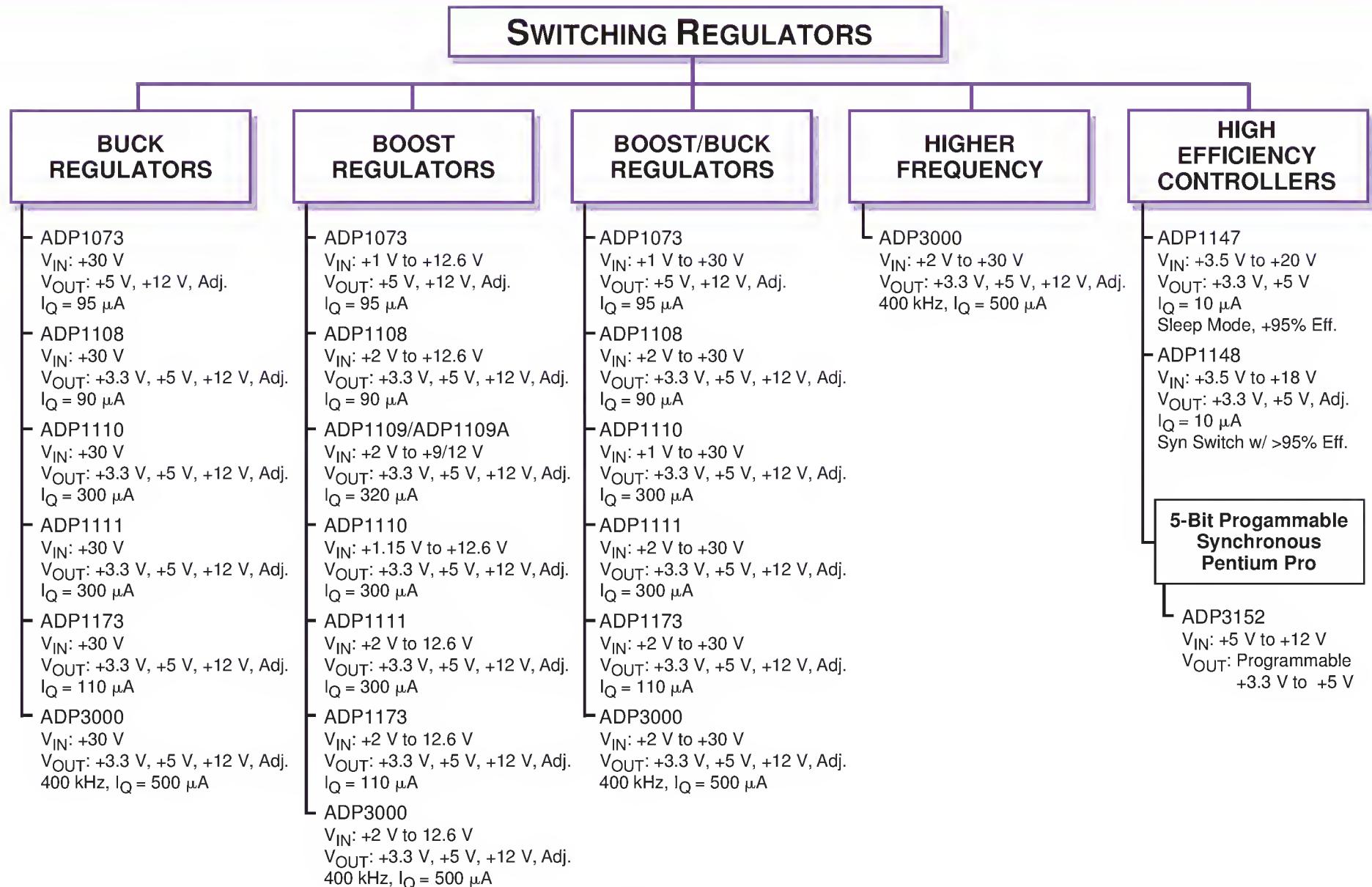
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POWER MANAGEMENT

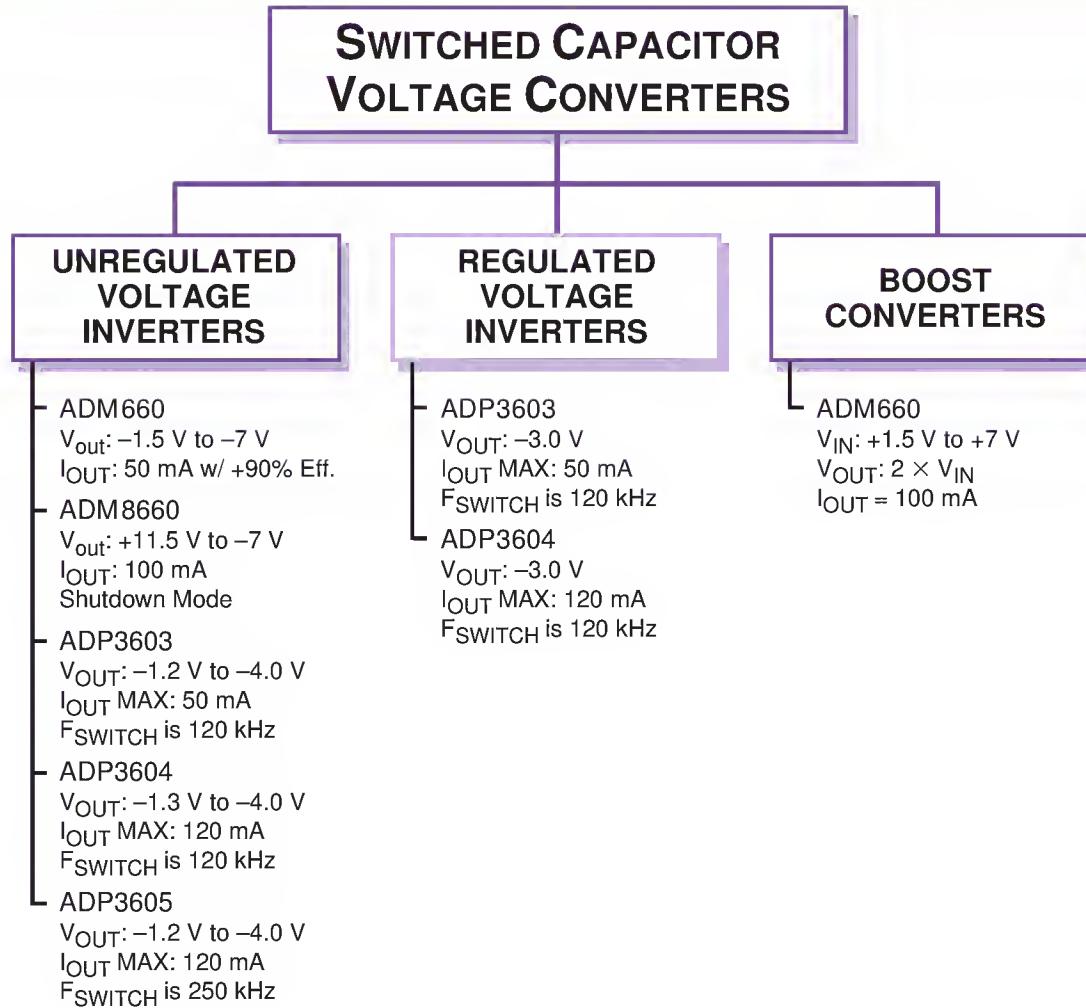
Low DROPOUT REGULATORS

| PRECISION anyCAP-LDOs | MULTIPLE OUTPUT anyCAP-LDOs | GENERAL PURPOSE LDOs | anyCAP in SOT-23 PACKAGE | LOW DROPOUT REGULATOR CONTROLLER |
|---|---|--|---|---|
| ADP3300/ADP3308 (50 mA) V_{IN} : +3 V to +16 V V_{OUT} : +2.7 V, +3 V, +3.2 V, +3.3 V, +5 V SOT-23: 6/5 Pins | ADP3302 (100 mA) V_{IN} : +3 V to +12 V V_{OUT} : +3 V, +3.2 V, +3.3 V, +5 V SO-8 Package, Dual Error Flag | ADM663A (100 mA) V_{IN} : +2 V to +16.5 V V_{OUT} : +3.3 V, +5 V, Adj. SO-8 Package | ADP3300/ADP3308 (50 mA) V_{IN} : +3 V to +16 V V_{OUT} : +2.7 V, +3 V, +3.2 V, +3.3 V, +5 V SOT-23: 6/5 Pins | ADP3310 (I_Q : 800 mA) V_{IN} : +2.5 V to +15 V V_{OUT} : +2.8 V, +3 V, +3.3 V, +5 V SO-8 Package |
| ADP3301 (100 mA) V_{IN} : +3 V to +12 V V_{OUT} : +2.7 V, +3 V, +3.2 V, +3.3 V, +5 V SO-8 Package | ADP3302 (100 mA) V_{IN} : +3 V to +12 V V_{OUT} : +3 V, +3.2 V, +3.3 V, +5 V SO-8 Package | ADM666A (100 mA) V_{IN} : +2 V to +16.5 V V_{OUT} : 3.3 V, +5 V, Adj. Low Battery Detection | ADP3307/ADP3309 (100 mA) V_{IN} : +3 V to +12 V V_{OUT} : +2.7 V, +3 V, +3.3 V SOT-23: 6/5 Pins | |
| ADP3302 (100 mA) V_{IN} : +3 V to +12 V V_{OUT} : +3 V, +3.2 V, +3.3 V, +5 V SO-8 Package | ADP3303 (200 mA) V_{IN} : +3.2 V to +12 V V_{OUT} : +2.7 V, +3 V, +3.2 V, +3.3 V, +5 V SO-8 Package | ADP667 (200 mA) V_{IN} : +3.5 V to +16.5 V V_{OUT} : +5 V, Adj. SO-8 Package | ADP3367 (300 mA) V_{IN} : +2.5 V to +16.5 V V_{OUT} : +5 V, Adj. SO-8 Package | |
| ADP3303 (200 mA) V_{IN} : +3.2 V to +12 V V_{OUT} : +2.7 V, +3 V, +3.2 V, +3.3 V, +5 V SO-8 Package | ADP3306 (300 mA) V_{IN} : +3.2 V to +12 V V_{OUT} : +2.7 V, +3 V, +3.2 V, +3.3 V, +5 V SO-8 and TSSOP Packages | ADP3367 (300 mA) V_{IN} : +2.5 V to +16.5 V V_{OUT} : +5 V, Adj. SO-8 Package | ADP3307/ADP3309 (100 mA) V_{IN} : +3 V to +12 V V_{OUT} : +2.7 V, +3 V, +3.3 V SOT-23: 6/5 Pins | |
| ADP3306 (300 mA) V_{IN} : +3.2 V to +12 V V_{OUT} : +2.7 V, +3 V, +3.2 V, +3.3 V, +5 V SO-8 and TSSOP Packages | ADP3307/ADP3309 (100 mA) V_{IN} : +3 V to +12 V V_{OUT} : +2.7 V, +3 V, +3.3 V SOT-23: 6/5 Pins | | | |

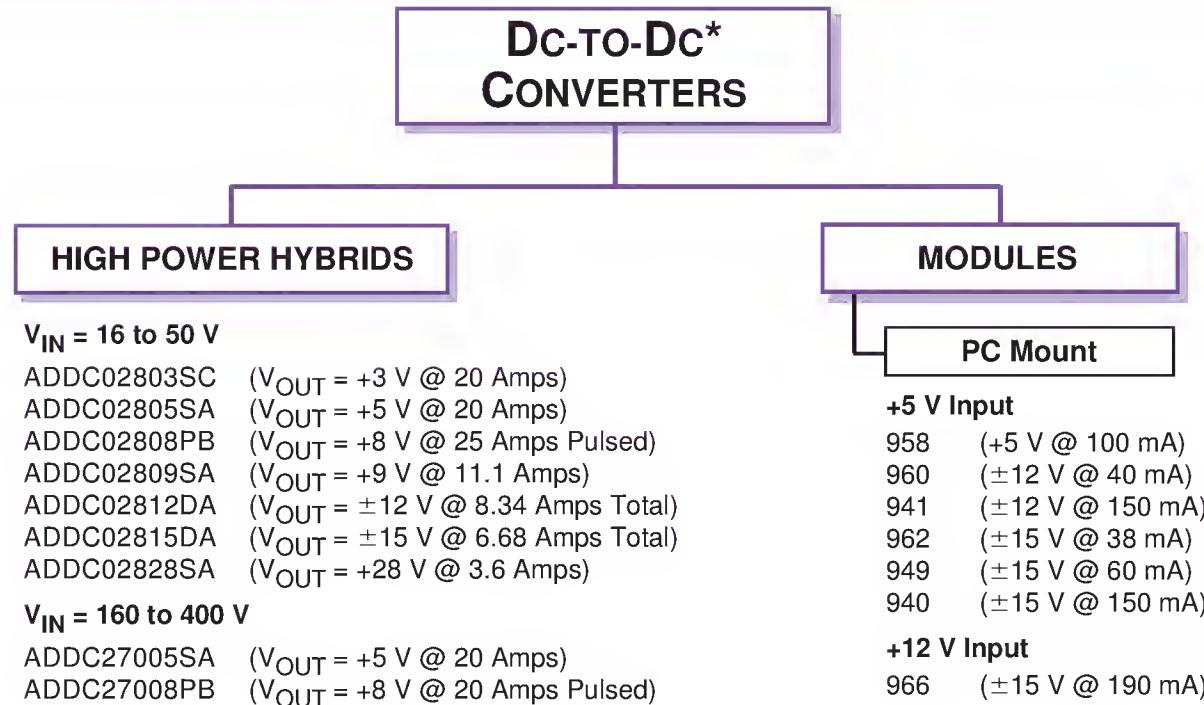
POWER MANAGEMENT



POWER MANAGEMENT

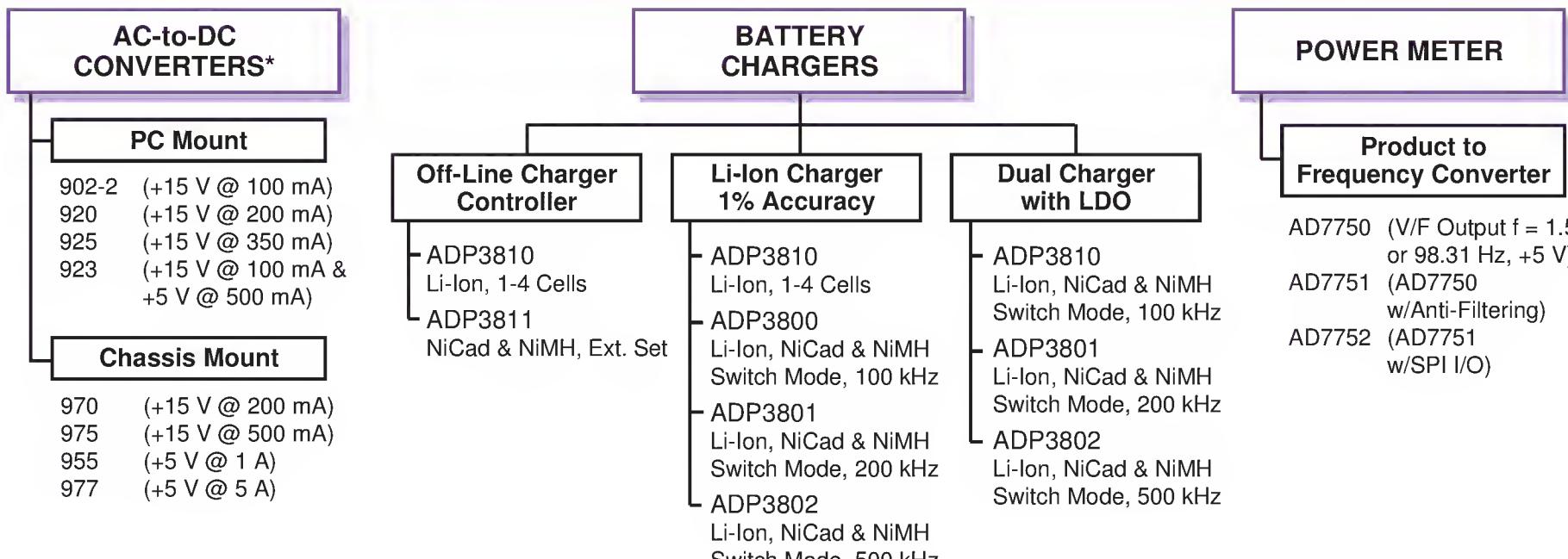


POWER MANAGEMENT



*Non-Monolithic Circuits

POWER MANAGEMENT

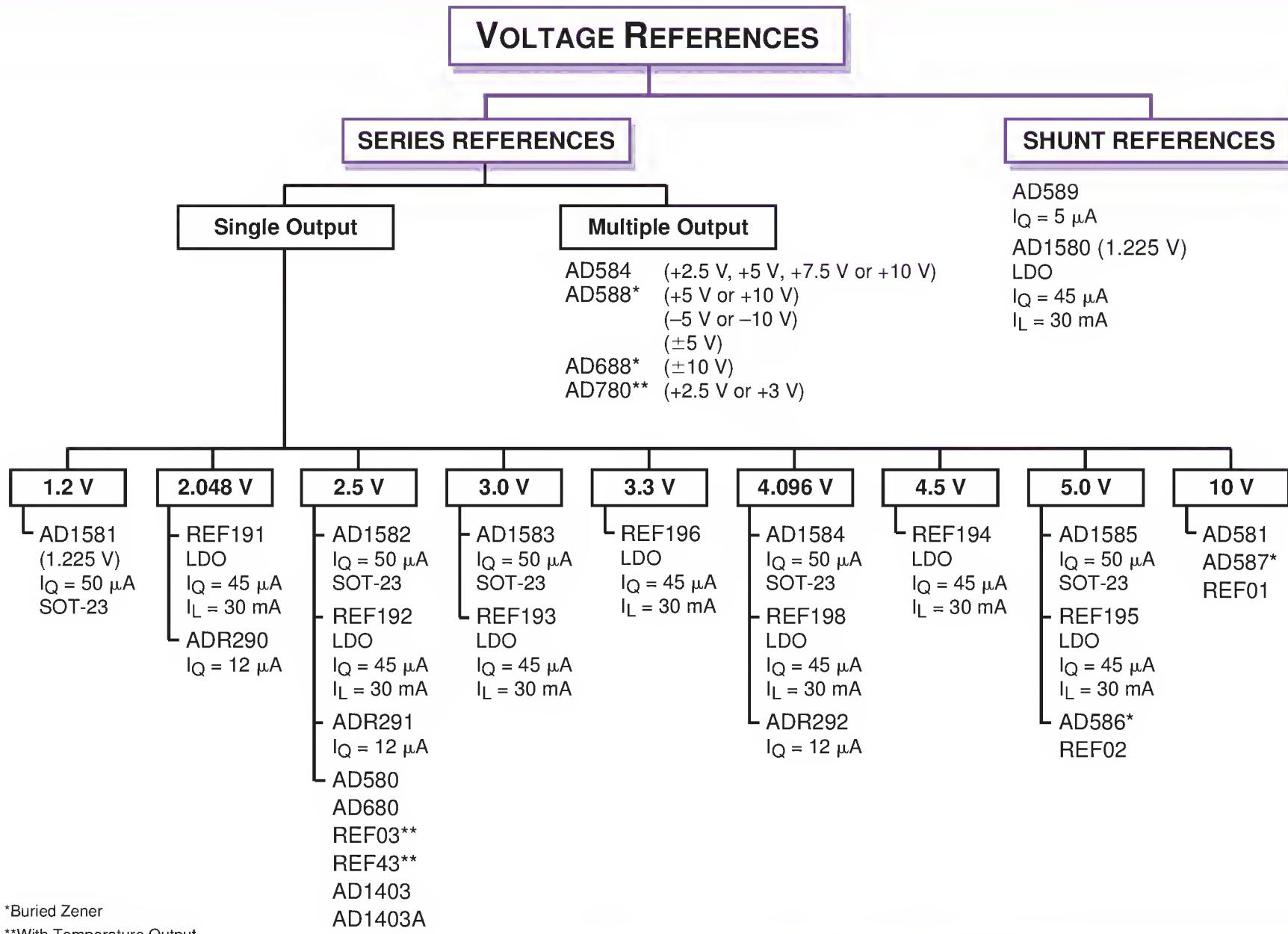


BATTERY BACKUP

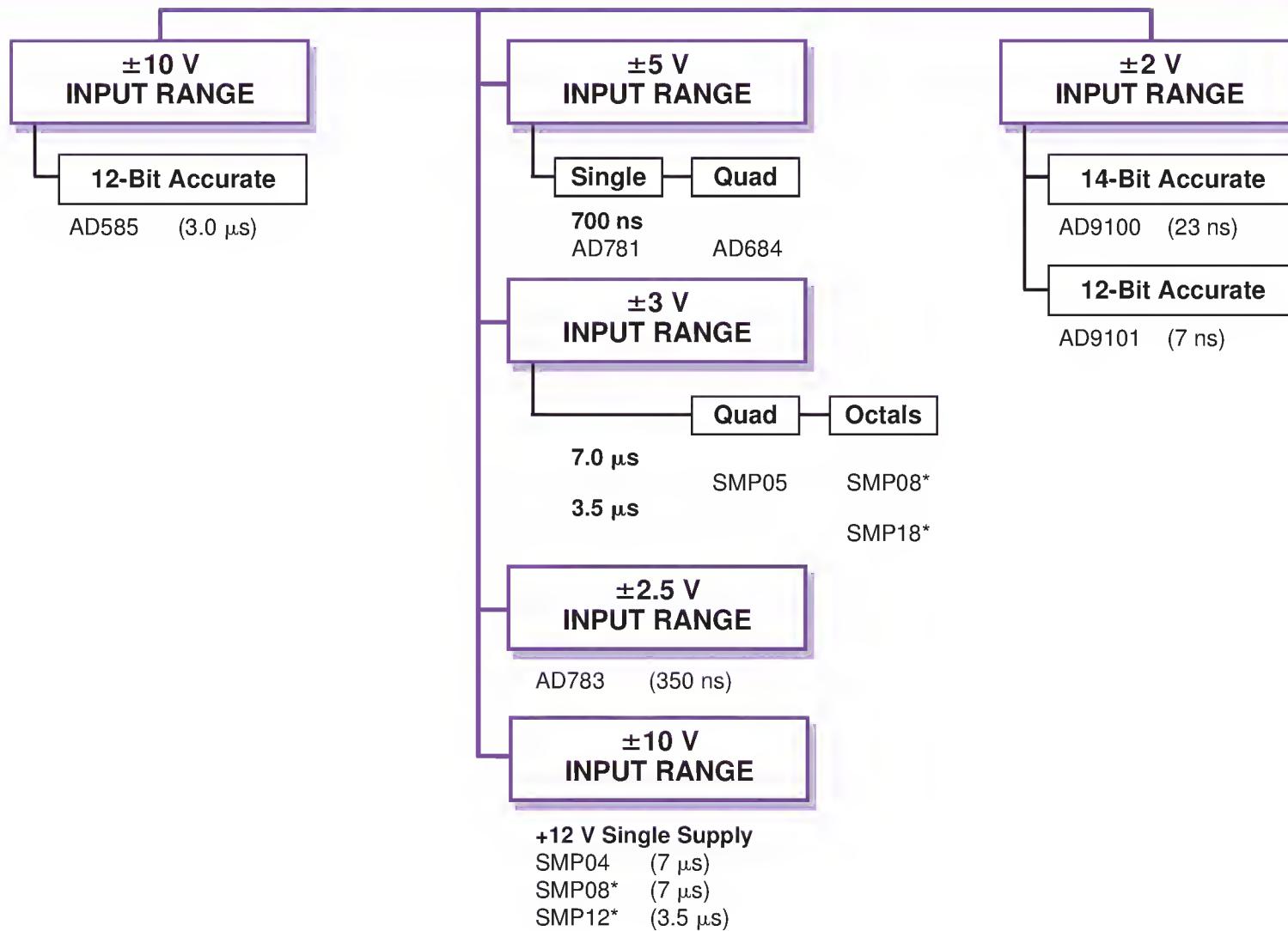
See *µProcessor Supervisory Circuits & Reset Generators Section.*

*Input Voltage 105 V AC to 125 V AC

REFERENCES

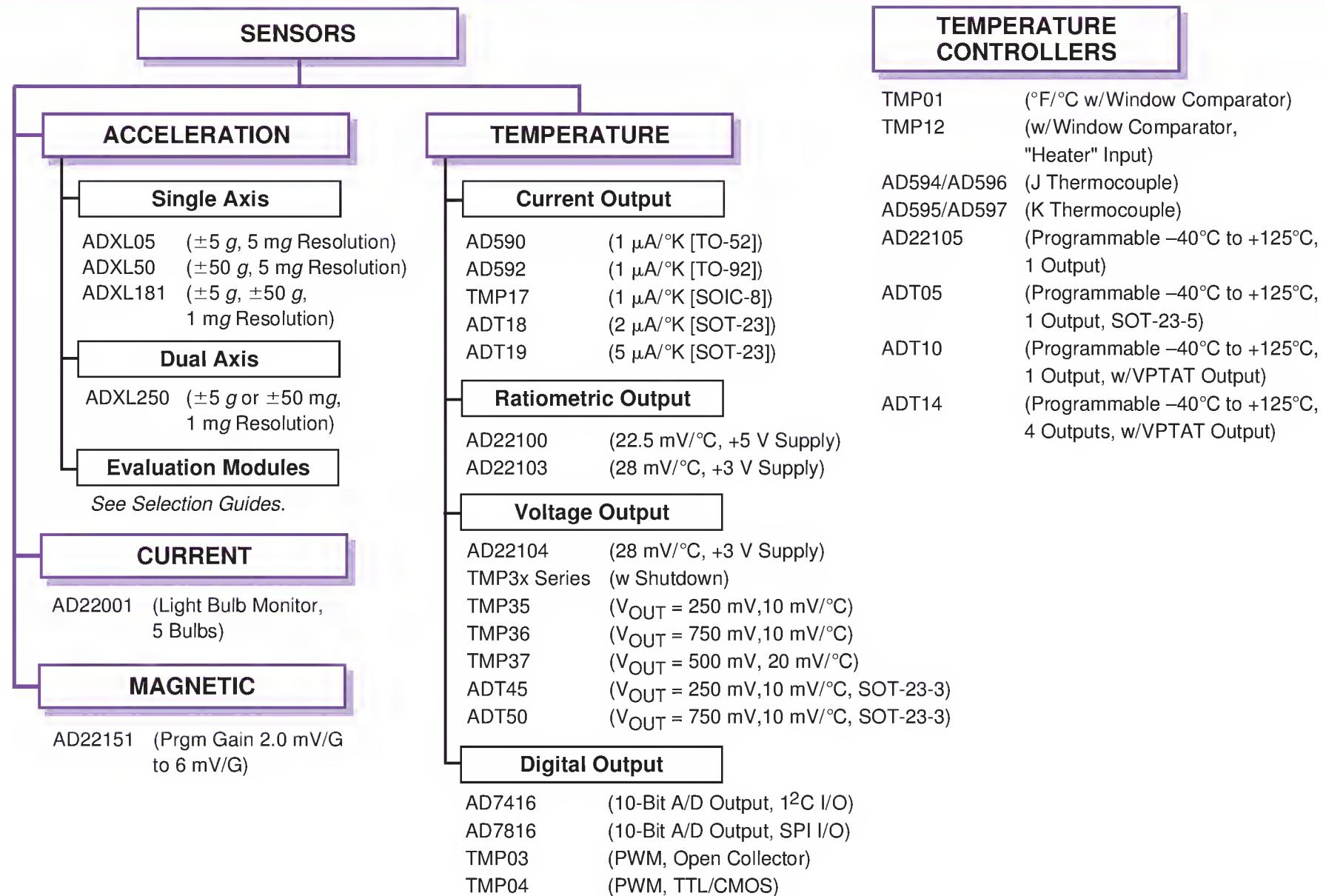


SAMPLE/TRACK & HOLD AMPLIFIERS

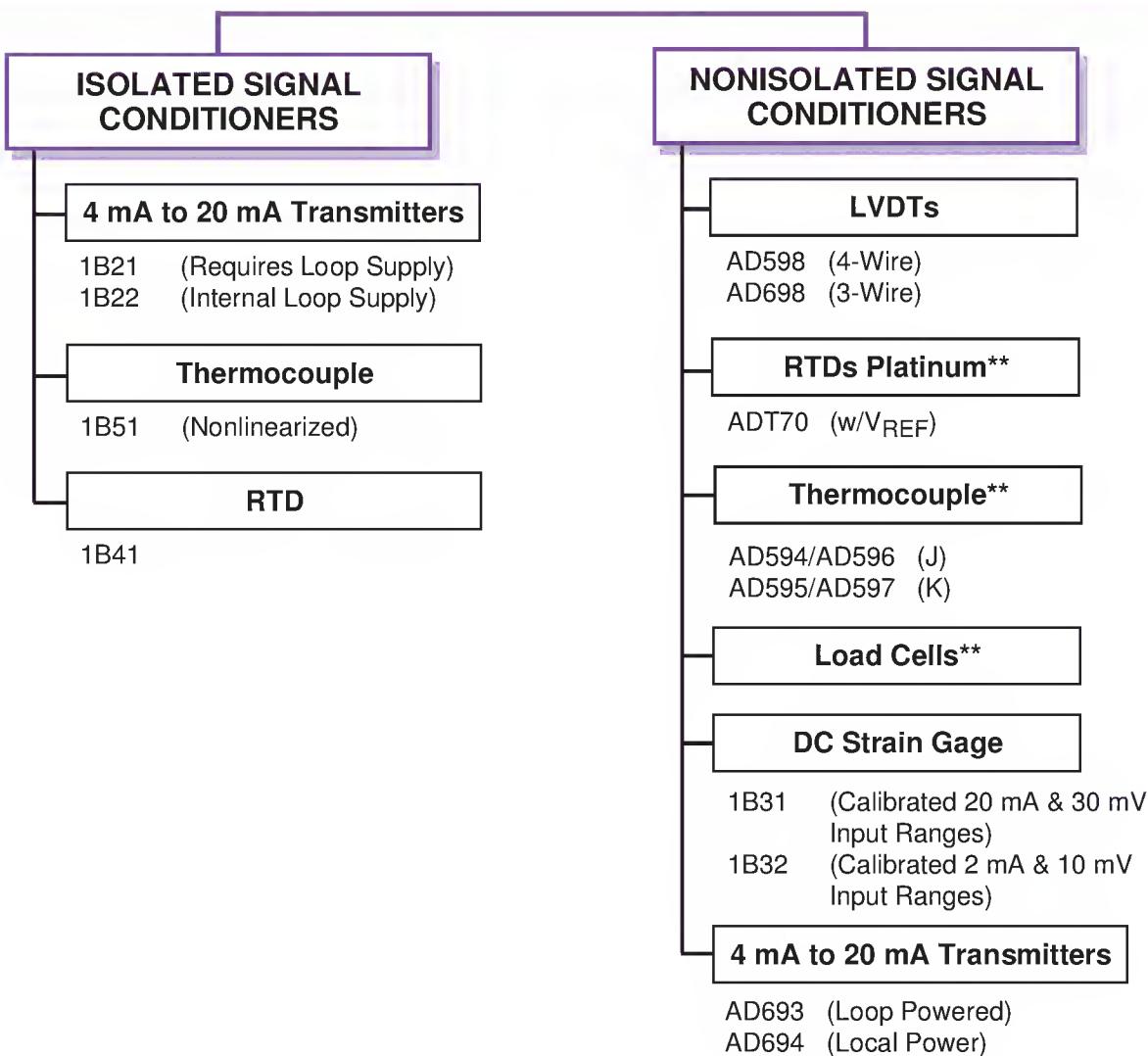


*One Input, Eight Outputs

SENSORS & SIGNAL CONDITIONERS



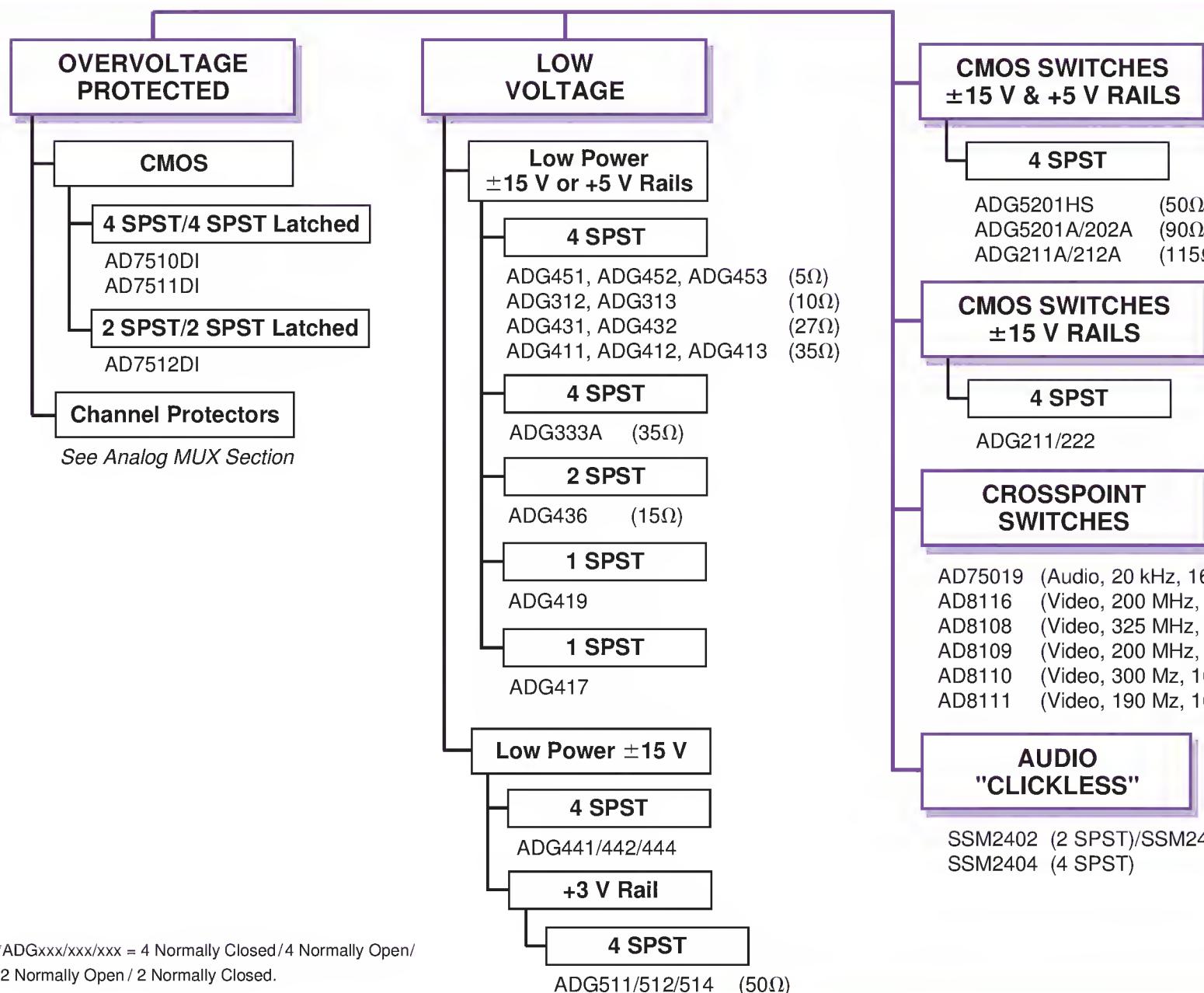
SENSORS & SIGNAL CONDITIONERS*



*See Industrial Catalog for 2B, 3B, 4B, 5B, 6B and 7B Series of Signal Conditioners

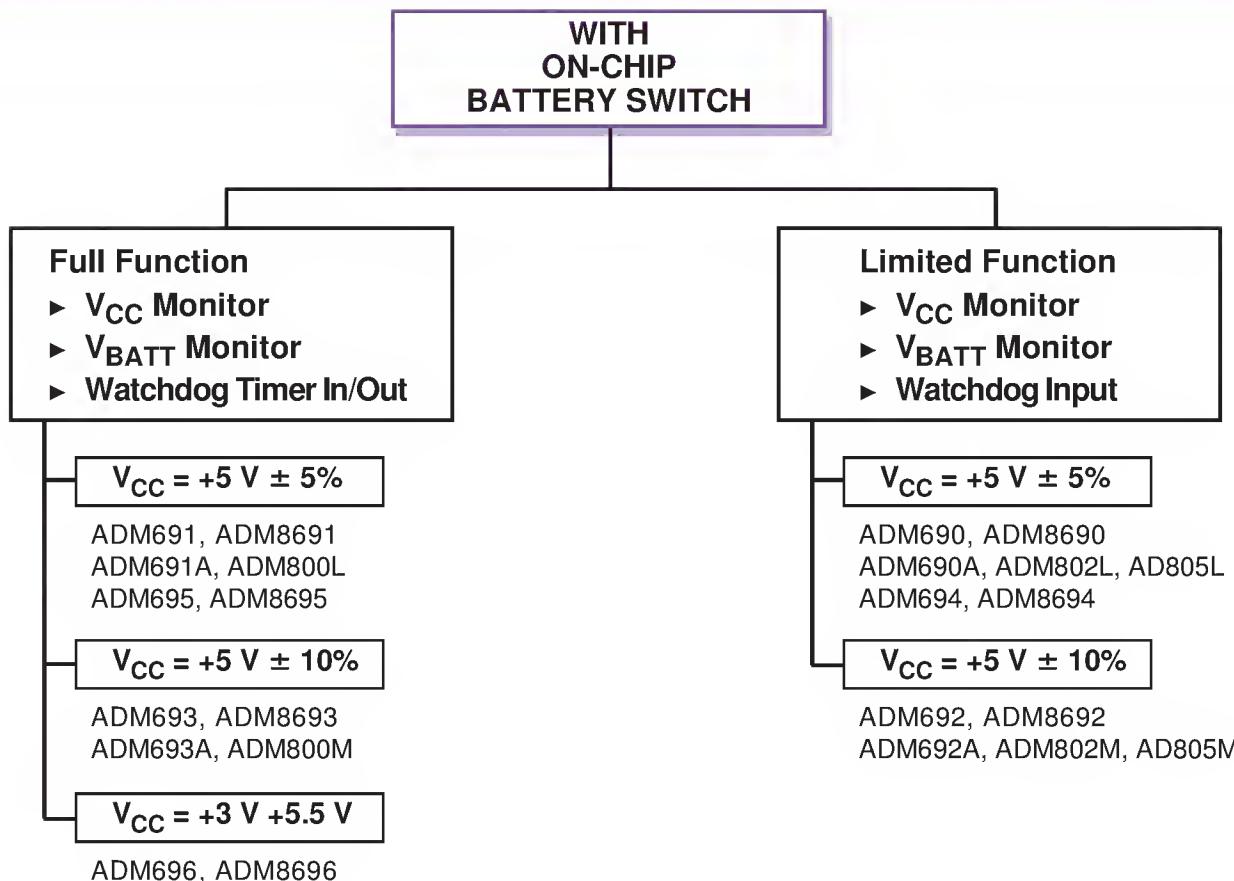
**See A/D Section, Sigma-Delta, for Signal Conditioning A/Ds.

SWITCHES

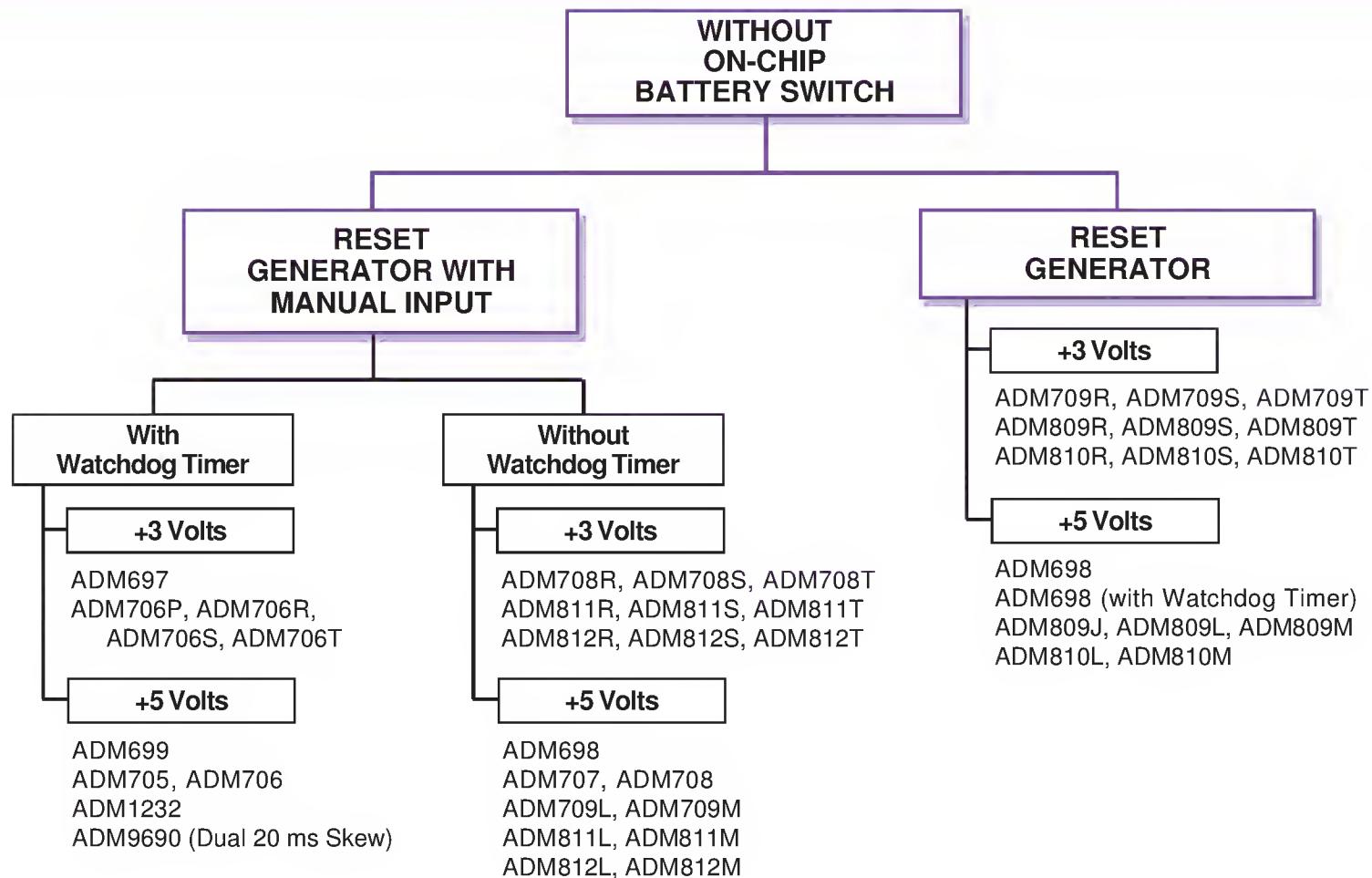


*ADGxxx/xxx/xxx = 4 Normally Closed/4 Normally Open/
2 Normally Open / 2 Normally Closed.

μ PROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS



μ PROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS



μ PROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS

FUNCTION TABLE

With On-Chip V_{BATT} Switch

| Circuit Functions | ADM8690 ADM690 | ADM8691 ADM691 | ADM8692 ADM692 | ADM8693 ADM693 | ADM8694 ADM694 | ADM8995 ADM695 | ADM8696 ADM696 | ADM805L ADM802L ADM690A | ADM800L ADM691A | ADM805M ADM802M ADM692A | ADM800M ADM693A |
|----------------------------------|-------------------|-------------------|-------------------|-------------------|-------------------|-------------------|-------------------|-------------------------------|--------------------|-------------------------------|--------------------|
| Fixed Power Up/Down Reset | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Variable Power Up/Down Reset | | | | | | | | ✓ | | | |
| Watchdog Timer Input | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Watchdog Timer Output | | ✓ | | ✓ | | ✓ | ✓ | | ✓ | | ✓ |
| Power Failing Warning In/Out | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ |
| Reset & Reset Outputs | | ✓ | | ✓ | | ✓ | ✓ | | ✓ | | ✓ |
| +3 V Systems | | | | | | | | | | | |
| CE In & CE Out | | ✓ | | ✓ | ✓ | ✓ | | | ✓ | | ✓ |
| Reset & Watchdog Timebase | | ✓ | | ✓ | | ✓ | ✓ | | ✓ | | ✓ |
| # of Pins | 8 | 16 | 8 | 16 | 8 | 16 | 16 | 8 | 16 | 8 | 16 |
| Low I _Q = 100 μ A | | | | | | | | ✓ | ✓ | ✓ | ✓ |
| Low Line Output | | ✓ | | ✓ | | ✓ | ✓ | | ✓ | | ✓ |

*SOT-23-3 Leads

μ PROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS

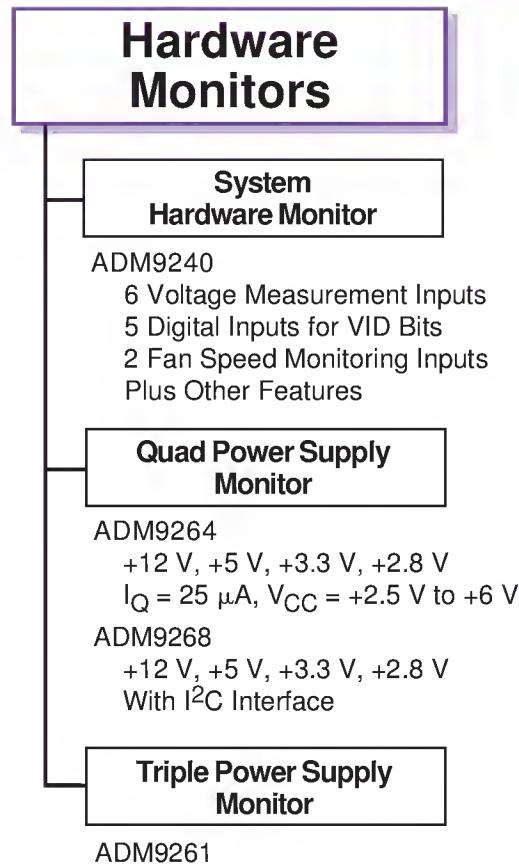
FUNCTION TABLE

Without On-Chip V_{BATT} Switch

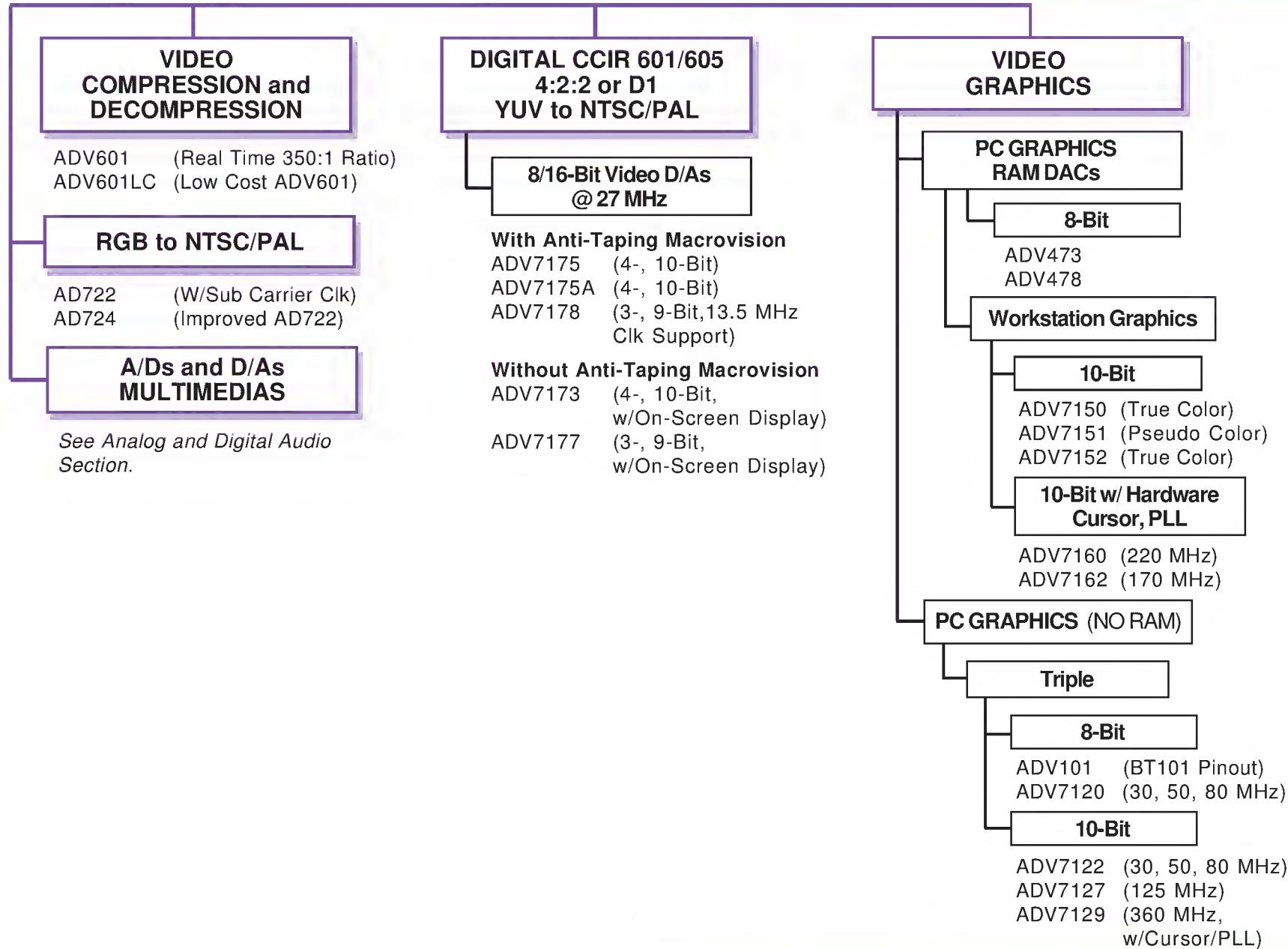
| Circuit Functions | ADM8697 ADM 697 | ADM 698 | ADM 699 | ADM 705 | ADM 706 | ADM 707 | ADM 708 | ADM 810* ADM 809* ADM 709 | ADM 9680 | ADM 1232 ADM 1232LP | ADM 811 ADM 812 |
|------------------------------|--------------------|---------|---------|---------|------------|---------|---------|---------------------------------|----------|------------------------|--------------------|
| Fixed Power Up/Down Reset | ✓ | ✓ | ✓ | ✓ | ✓ | ✓ | | ✓ | ✓ | ✓ | ✓ |
| Variable Power Up/Down Reset | ✓ | | | | | | | | | | |
| Low Line Output | ✓ | | | | | | | | | | |
| Watchdog Timer Input | ✓ | | ✓ | ✓ | ✓ | | | | ✓ | ✓ | ✓ |
| Watchdog Timer Output | ✓ | | ✓ | ✓ | ✓ | | | | | | |
| Power Failing Warning In/Out | ✓ | | | ✓ | ✓ | ✓ | ✓ | | | | |
| Reset & Watchdog Timebase | ✓ | | | ✓ | ✓ | | | | ✓ | | |
| Manual Reset | | | | ✓ | ✓ | ✓ | ✓ | | | ✓ | ✓ |
| +3 V Systems | ✓ | | | | P, R, S, T | | R, S, T | R, S, T | | | R, S, T |
| CE Out | ✓ | | | | | | | | | | |
| # of Pins | 16 | 8/16 | 16 | 8 | 8 | 8 | 8 | 3/8 | 8 | 8/16 | 4 |
| Reset & Reset Outputs | | | ✓ | | | ✓ | | | ✓ | | |

*SOT-23-3 Leads

μ PROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS



VIDEO AND MULTIMEDIA



V/F & F/V CONVERTERS

V/F and F/V CONVERTERS

AD537 (150 kHz, w/Sq Wave Output)
AD654 (500 kHz, w/Sq Wave Output)
ADVFC32 (500 kHz)
AD650 (1 MHz)

Synchronous

AD652 (2 MHz Clock)
AD7741 (5 MHz Clock)
AD7742 (5 MHz Clock, Bipolar Input)

Power Meter

See Power Management Section.